

What Each FEB2 BPOL12V Circuit Powers

BPOL12V Load	# Control IpGBTs	# ALFE2 2.5V LDOs	# 2.5V Outputs	# VTRx+	#Data IpGBTs	#CV4+ALFE2s
Slice 1,2 1.4V	0	0	0	0	0	2
Slice 3,4 1.4V	0	0	0	0	0	2
Slice 5,6 1.4V	0	0	0	0	1	2
Slice 7,8 1.4V	0	0	0	0	0	2
Slice 9,10 1.4V	0	0	0	0	1	2
Slice 11,12 1.4V	0	0	0	0	0	2
Slice 13,14 1.4V	0	0	0	0	0	2
Slice 15,16 1.4V	0	0	0	0	1	2
ALFE2 3.5V left	0	1	0	0	0	0
ALFE2 3.5V right	0	1	0	0	0	0
Control IpGBTs 1.4V	2	0	0	2	0	0
VTRx+ 2.5V	0	0	0	1	0	0
Slice 17,18 1.4V	0	0	0	0	1	2
Slice 19,20 1.4V	0	0	0	0	0	2
Slice 21,22 1.4V	0	0	0	0	0	2
Slice 23,24 1.4V	0	0	0	1	2	2
Slice 25,26 1.4V	0	0	0	0	0	2
Slice 27,28 1.4V	0	0	0	0	0	2
Slice 29,30 1.4V	0	0	0	1	2	2
Slice 31,32 1.4V	0	0	0	0	1	2

LED: IpGBT

LED: bPOL12

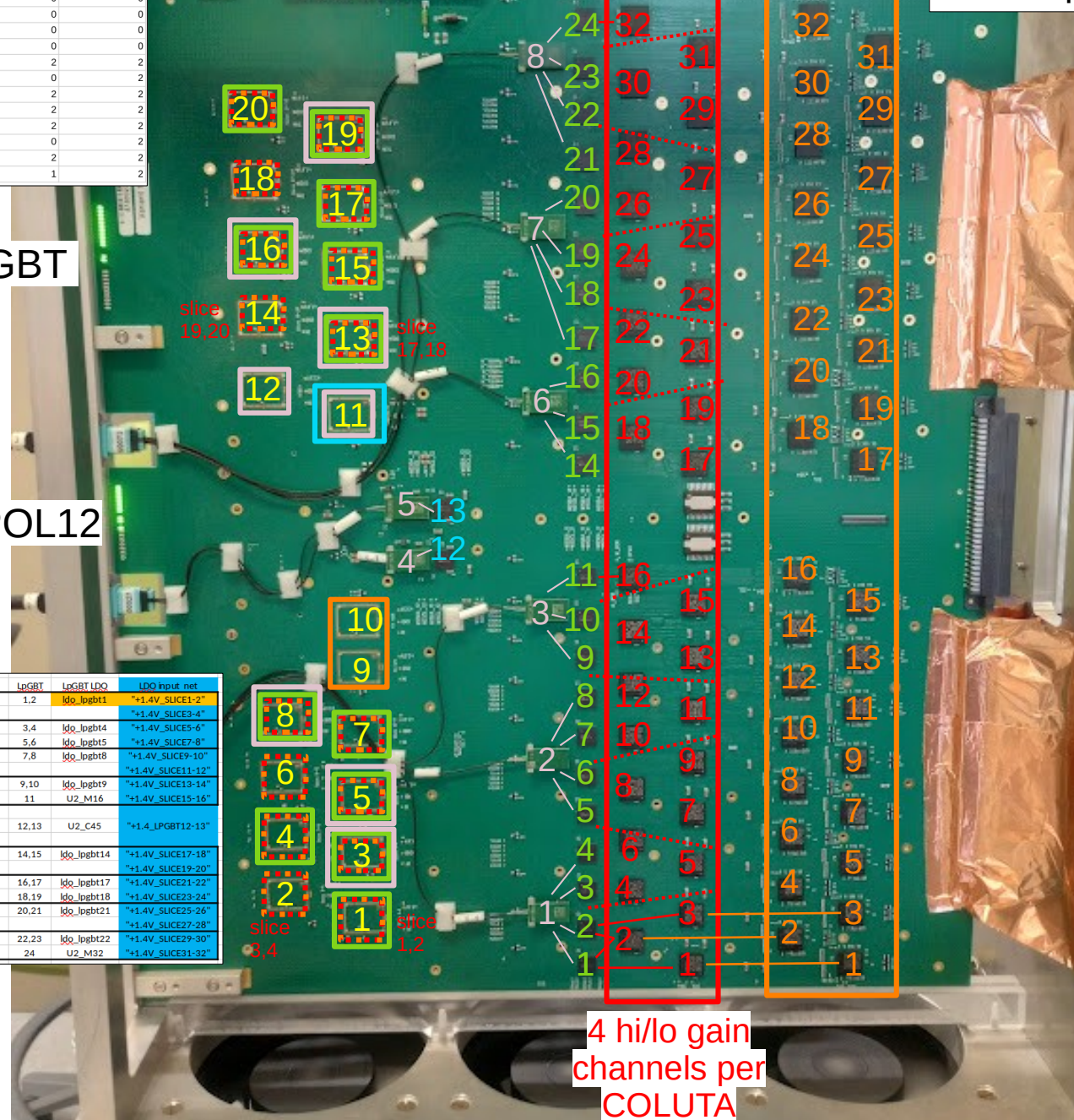
		Net	LDO input net	LDO	PA	ADC		LpGBT	LpGBT LDO	LDO input net
pol12v_slice1-4	A (low row)	"~+1.4V_SLICE1-4_A"	"~+1.4V_SLICE1-2"	1,2	1,2	1,2	1,2	ldo_ipgbt1	"~+1.4V_SLICE1-2"	
	B (upper row)	"~+1.4V_SLICE1-4_B"	"~+1.4V_SLICE3-4"	3,4	3,4	3,4			"~+1.4V_SLICE3-4"	
pol12v_slice5-8	A	"~+1.4V_SLICES-8_A"	"~+1.4V_SLICES-6"	5,6	5,6	5,6	3,4	ldo_ipgbt4	"~+1.4V_SLICES-6"	
	B	"~+1.4V_SLICES-8_B"	"~+1.4V_SLICES-7"	7,8	7,8	7,8	5,6	ldo_ipgbt5	"~+1.4V_SLICES-7"	
pol12v_slice9-12	A	"~+1.4V_SLICE9-12_A"	"~+1.4V_SLICE9-10"	9,10	9,10	9,10	7,8	ldo_ipgbt8	"~+1.4V_SLICE9-10"	
	B	"~+1.4V_SLICE9-12_B"	"~+1.4V_SLICE11-12"	11,12	11,12	11,12			"~+1.4V_SLICE11-12"	
pol12v_slice13-16	A	"~+1.4V_SLICE13-16_A"	"~+1.4V_SLICE13-14"	13,14	13,14	13,14	9,10	ldo_ipgbt9	"~+1.4V_SLICE13-14"	
	B	"~+1.4V_SLICE13-16_B"	"~+1.4V_SLICE15-16"	15,16	15,16	15,16	11	U2_M16	"~+1.4V_SLICE15-16"	
POL12V_ipgbt12-13		"~+1.4_IPGBT12-13"					12,13	U2_C45	"~+1.4_IPGBT12-13"	
pol12v_slice17-20	A	"~+1.4V_SLICE17-20_A"	"~+1.4V_SLICE17-18"	17,18	17,18	17,18	14,15	ldo_ipgbt14	"~+1.4V_SLICE17-18"	
	B	"~+1.4V_SLICE17-20_B"	"~+1.4V_SLICE19-20"	19,20	19,20	19,20			"~+1.4V_SLICE19-20"	
pol12v_slice21-24	A	"~+1.4V_SLICE21-24_A"	"~+1.4V_SLICE21-22"	21,22	21,22	21,22	16,17	ldo_ipgbt17	"~+1.4V_SLICE21-22"	
	B	"~+1.4V_SLICE21-24_B"	"~+1.4V_SLICE23-24"	23,24	23,24	23,24	18,19	ldo_ipgbt18	"~+1.4V_SLICE23-24"	
pol12v_slice25-28	A	"~+1.4V_SLICES-28_A"	"~+1.4V_SLICES-26"	25,26	25,26	25,26	20,21	ldo_ipgbt21	"~+1.4V_SLICES-26"	
	B	"~+1.4V_SLICES-28_B"	"~+1.4V_SLICES-27"	27,28	27,28	27,28			"~+1.4V_SLICES-27"	
pol12v_slice29-32	A	"~+1.4V_SLICE29-32_A"	"~+1.4V_SLICE29-30"	29,30	29,30	29,30	22,23	ldo_ipgbt22	"~+1.4V_SLICE29-30"	
	B	"~+1.4V_SLICE29-32_B"	"~+1.4V_SLICE31-32"	30,31	30,31	30,31	24	U2_M32	"~+1.4V_SLICE31-32"	

Data
IpGBT
Control
IpGBT

COLUTA
ADC

ALFE
preamp

This is a view of the **top/front** side of the board, i.e. with custom components



4 hi/lo gain
channels per
COLUTA