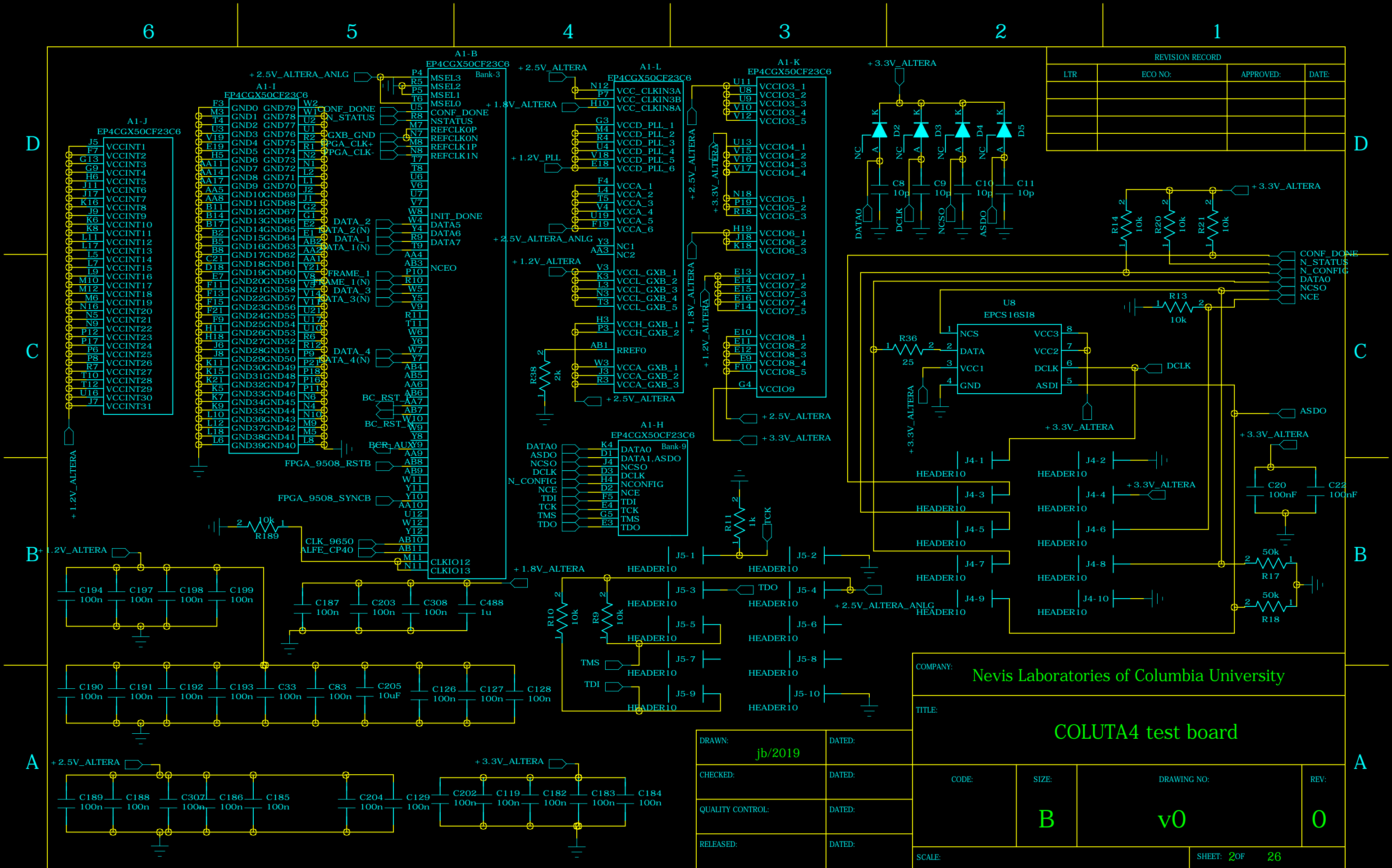


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LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Nevis Laboratories of Columbia University			
TITLE: COLUTAV3 test board			
CODE:	SIZE:	DRAWING NO:	REV:
SCALE:			SHEET: 1 OF 10



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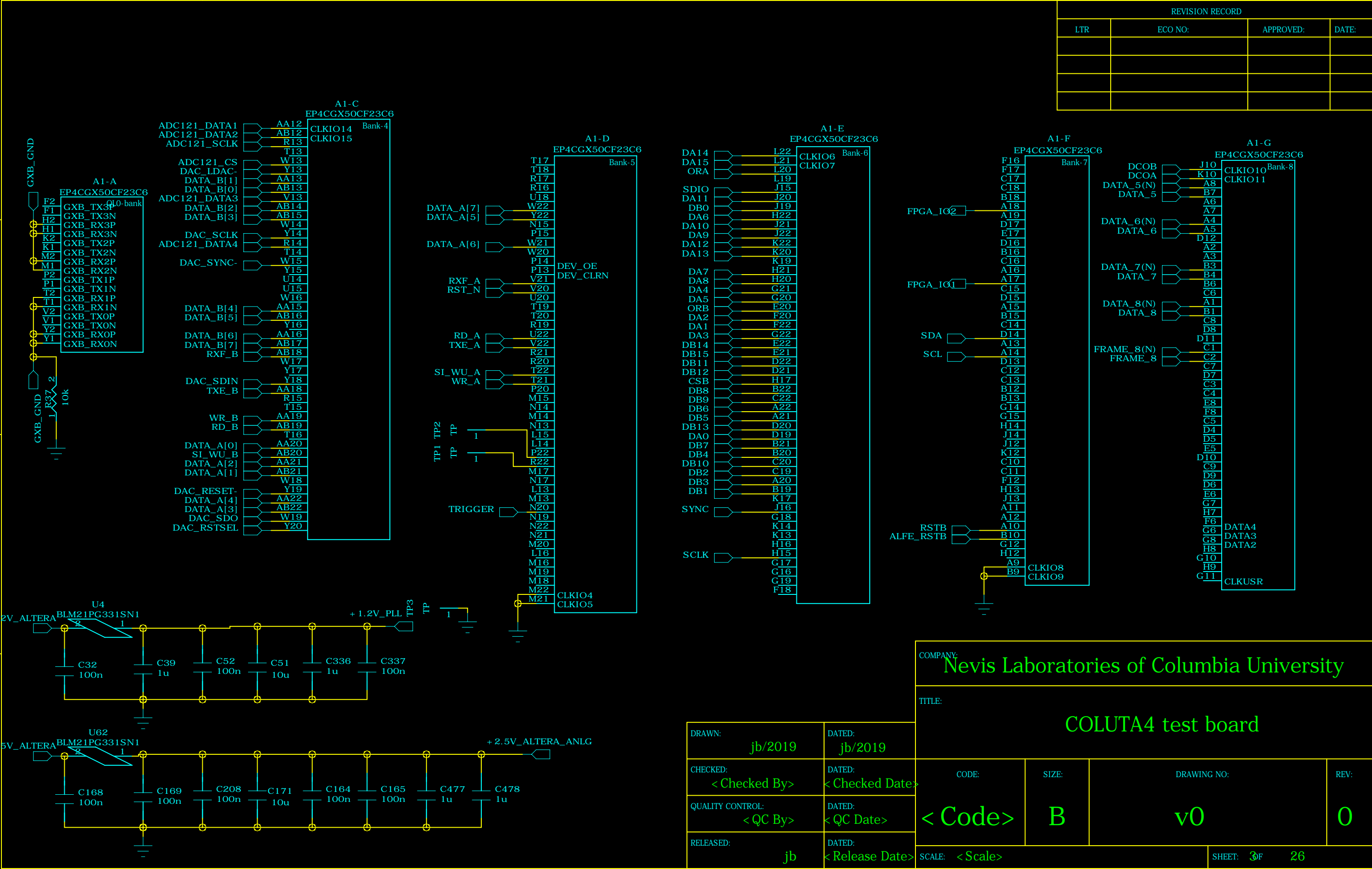
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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COMPANY: Nevis Laboratories of Columbia University

TITLE: COLUTA4 test board

DRAWN:	jb/2019	DATED:	jb/2019
CHECKED:	< Checked By >	DATED:	< Checked Date >
QUALITY CONTROL:	< QC By >	DATED:	< QC Date >
RELEASED:	jb	DATED:	< Release Date >

CODE:	SIZE:	DRAWING NO:	REV:
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SCALE: < Scale >			SHEET: 3F 26

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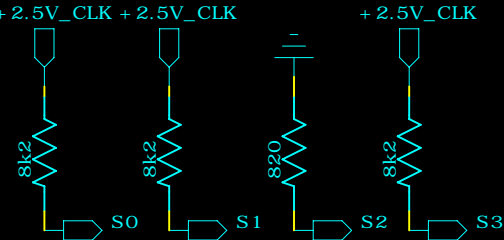
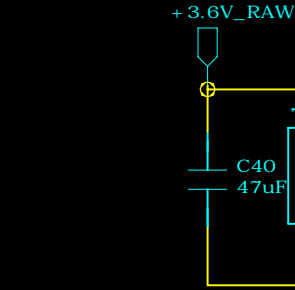
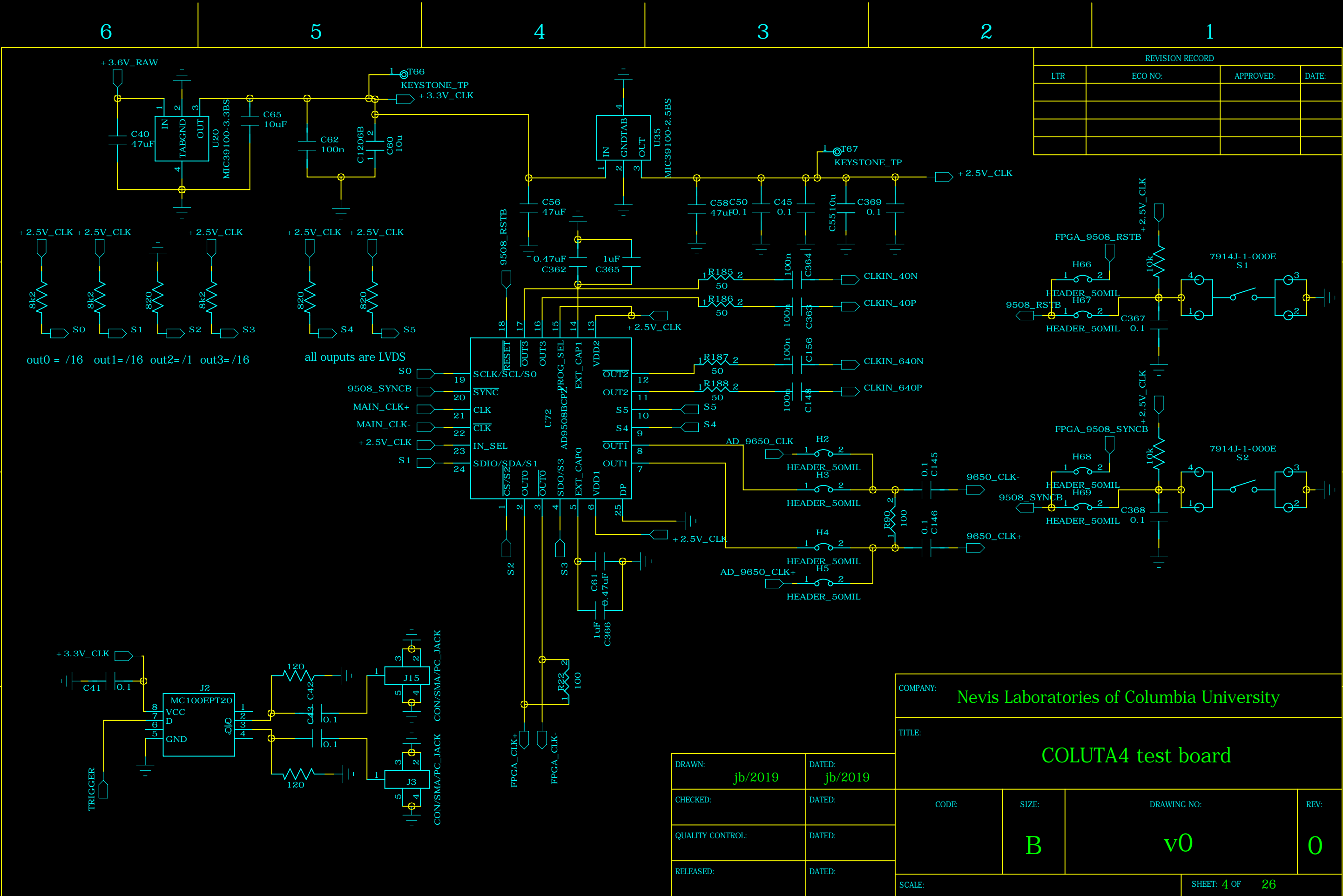
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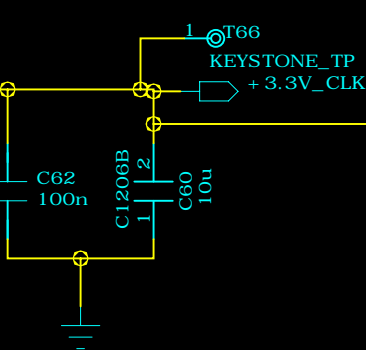
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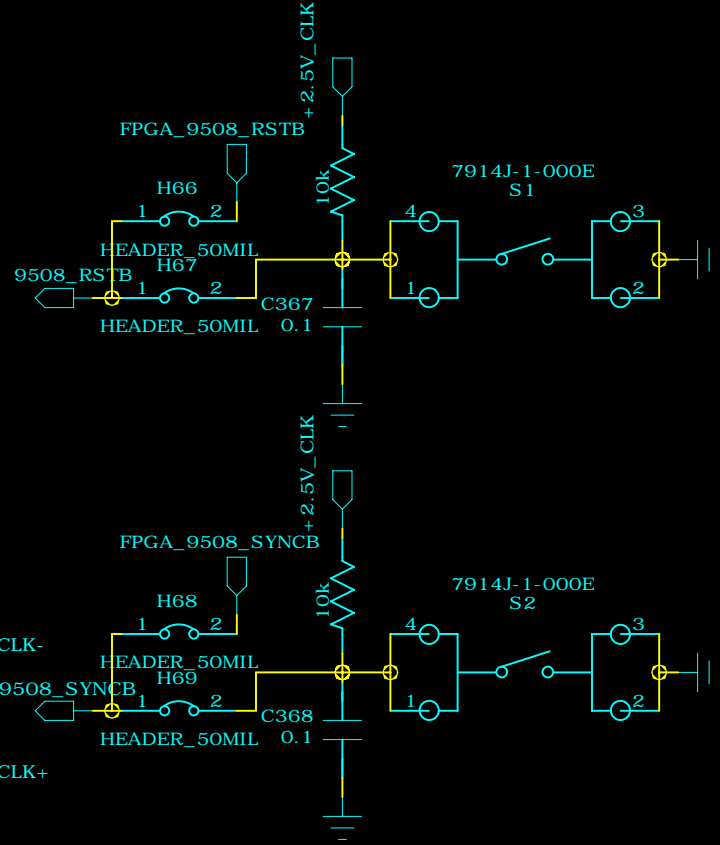
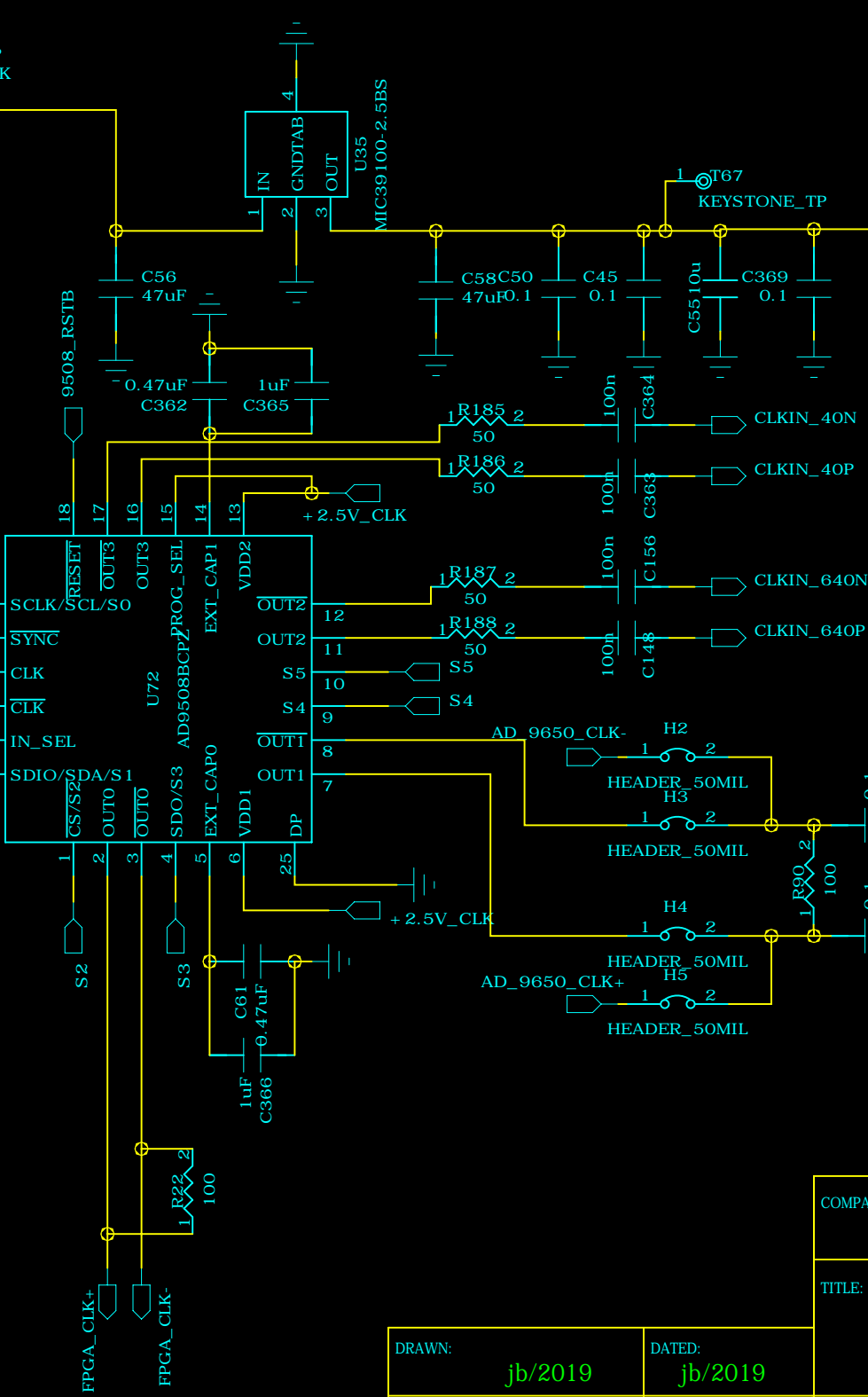
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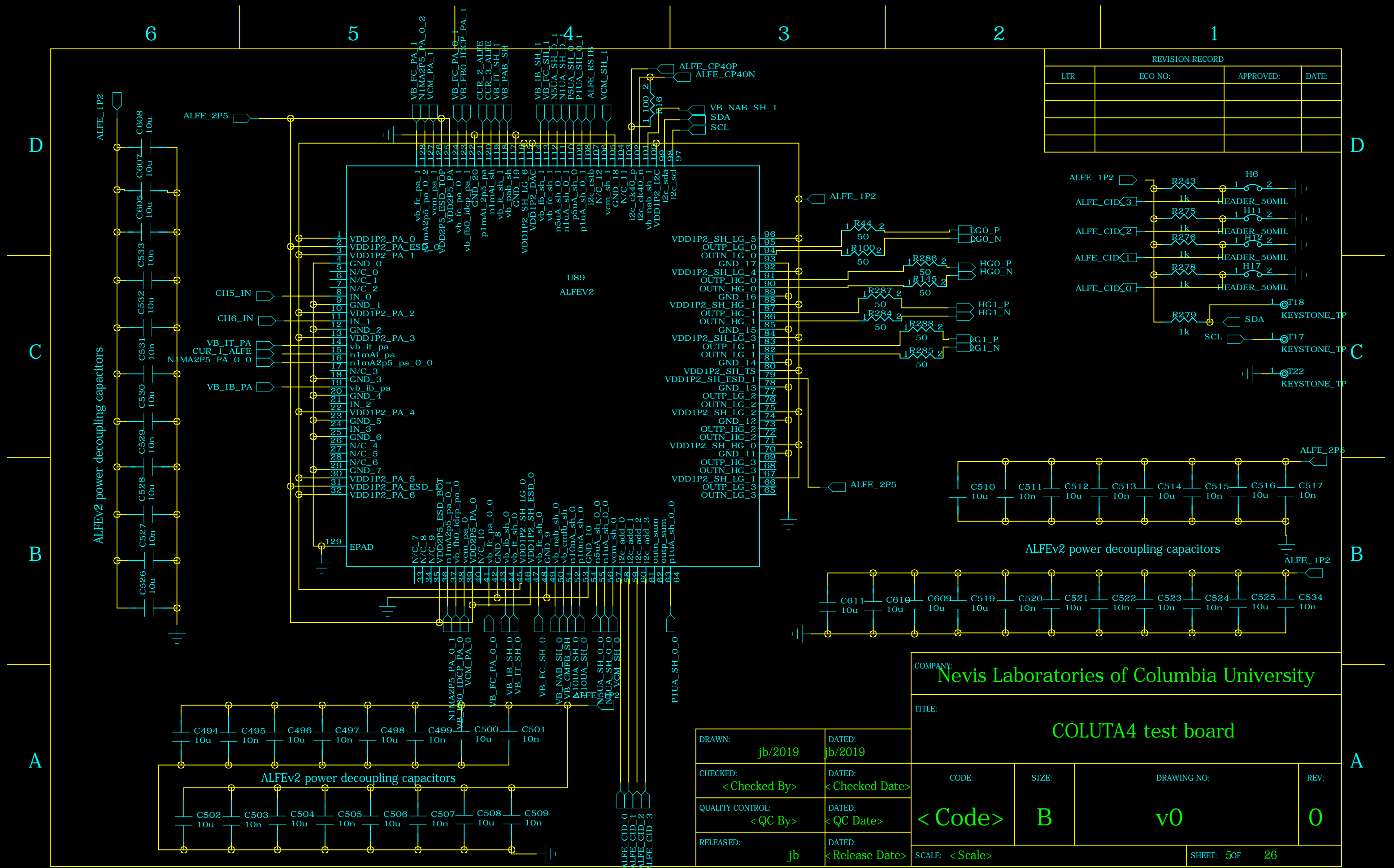


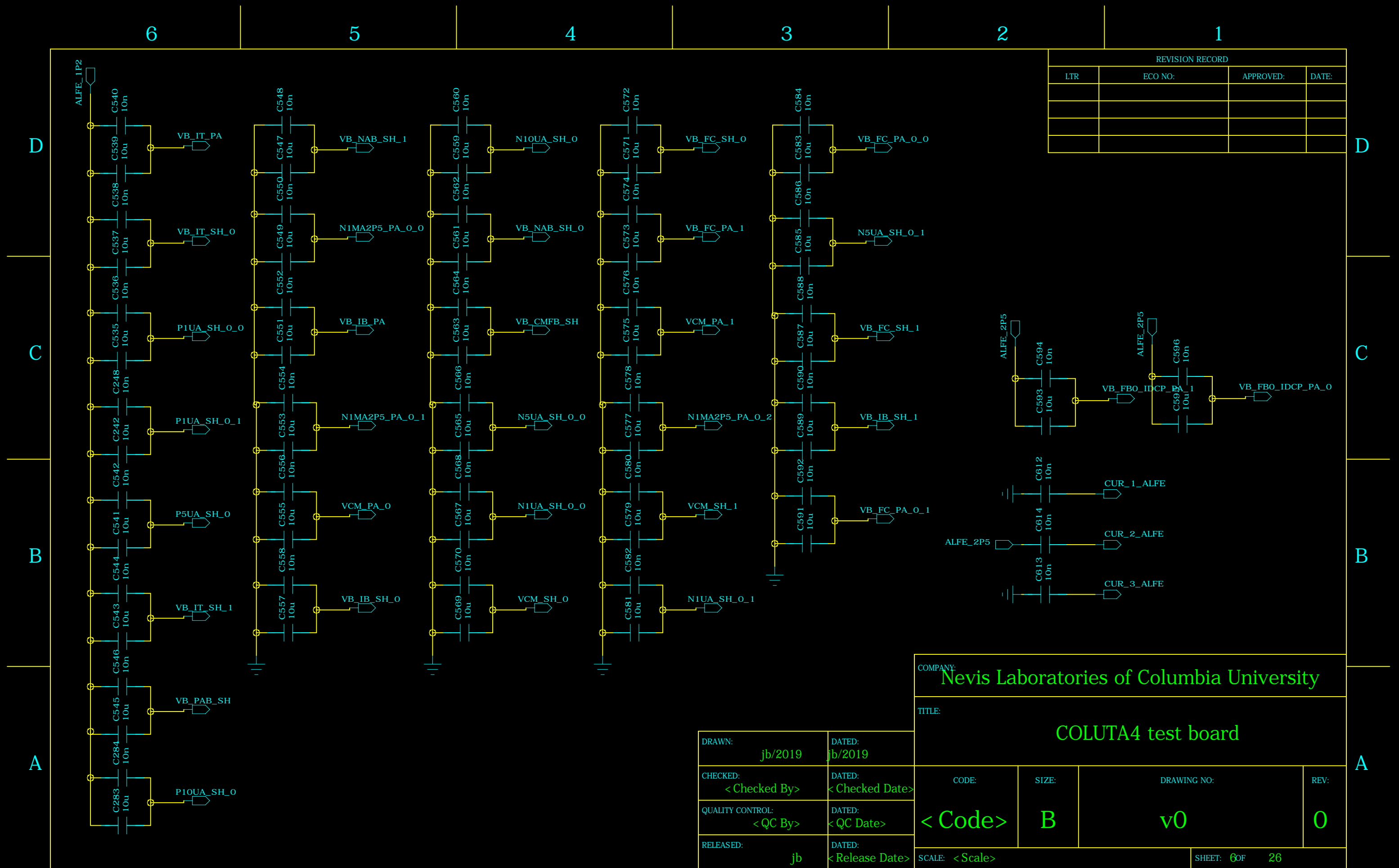
out0 = /16 out1=/16 out2=/1 out3=/16

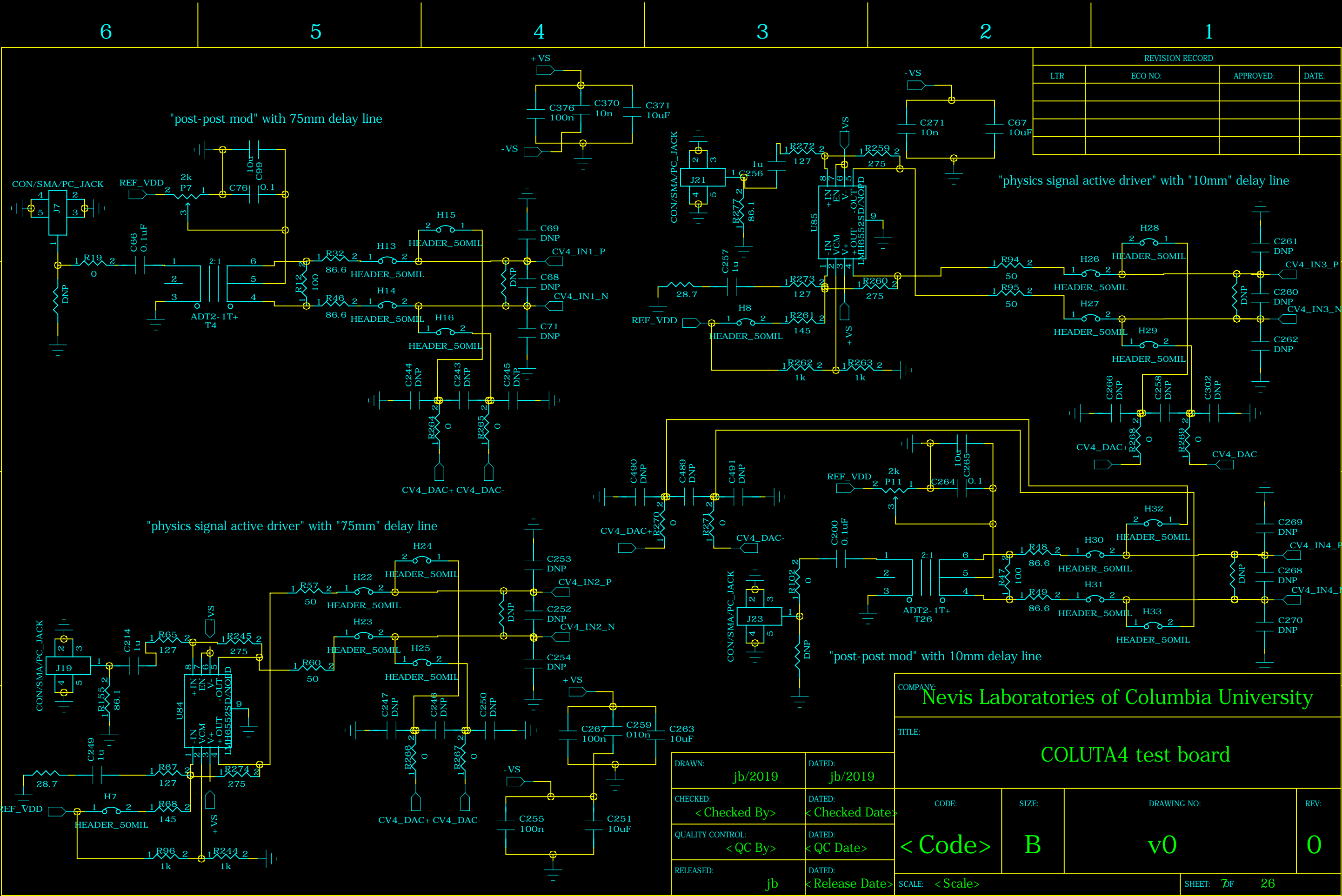


all outputs are LVDS









REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Nevis Laboratories of Columbia University			
TITLE: COLUTA4 test board			
CODE: Code	SIZE: B	DRAWING NO: v0	REV: 0
SCALE: < Scale >			SHEET: 7DF 26



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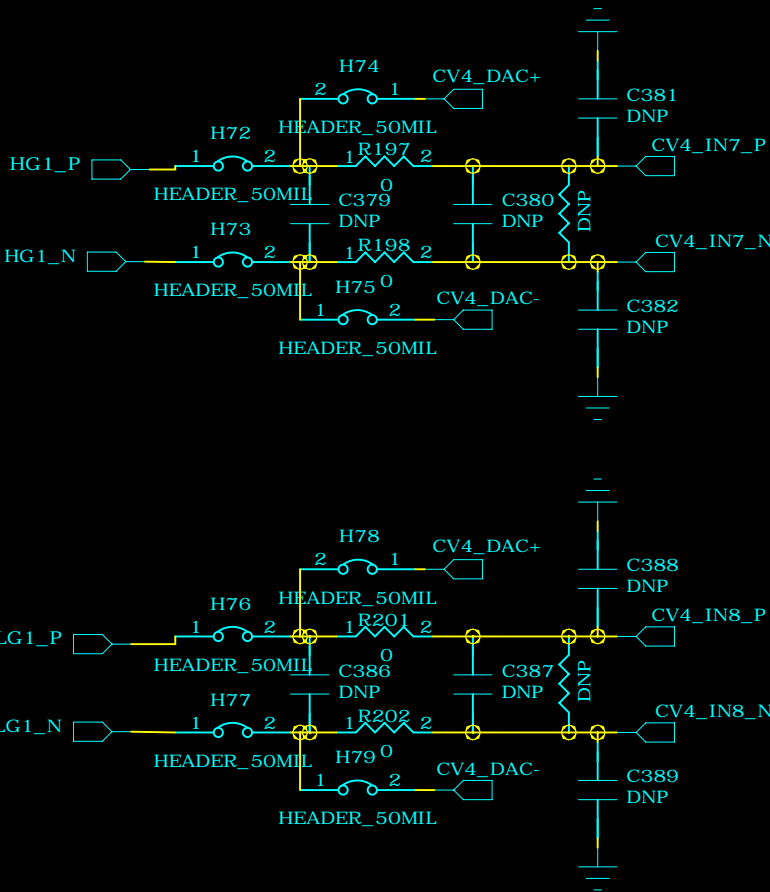
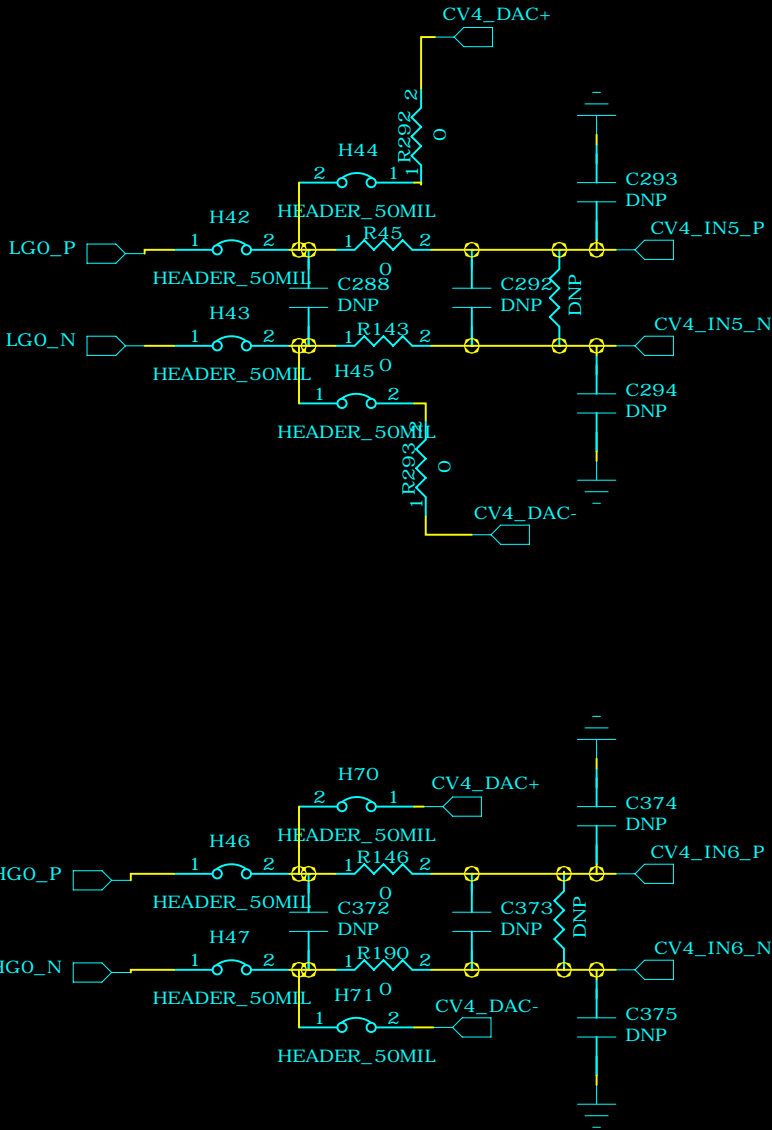
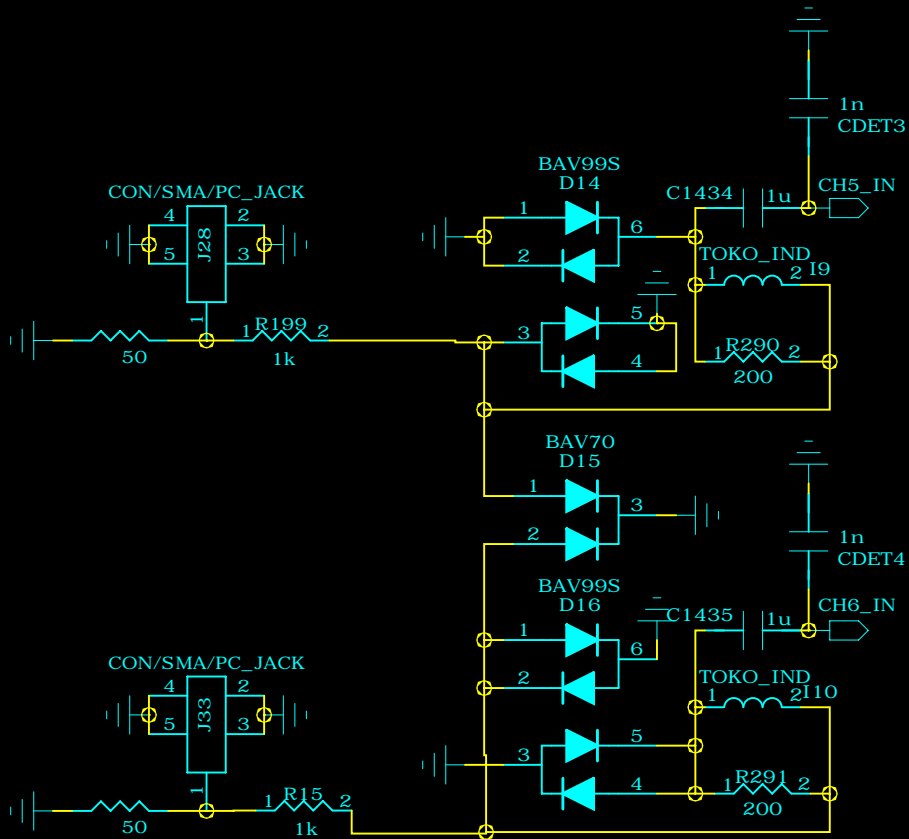
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COMPANY: Nevis Laboratories of Columbia University

TITLE: COLUTA4 test board

DRAWN:	j b/2019	DATED:	j b/2019
CHECKED:	< Checked By >	DATED:	< Checked Date >
QUALITY CONTROL:	< QC By >	DATED:	< QC Date >
RELEASED:	j b	DATED:	< Release Date >

CODE:	SIZE:	DRAWING NO:	REV:
< Code >	B	v0	0
SCALE: < Scale >			SHEET: 8 of 26

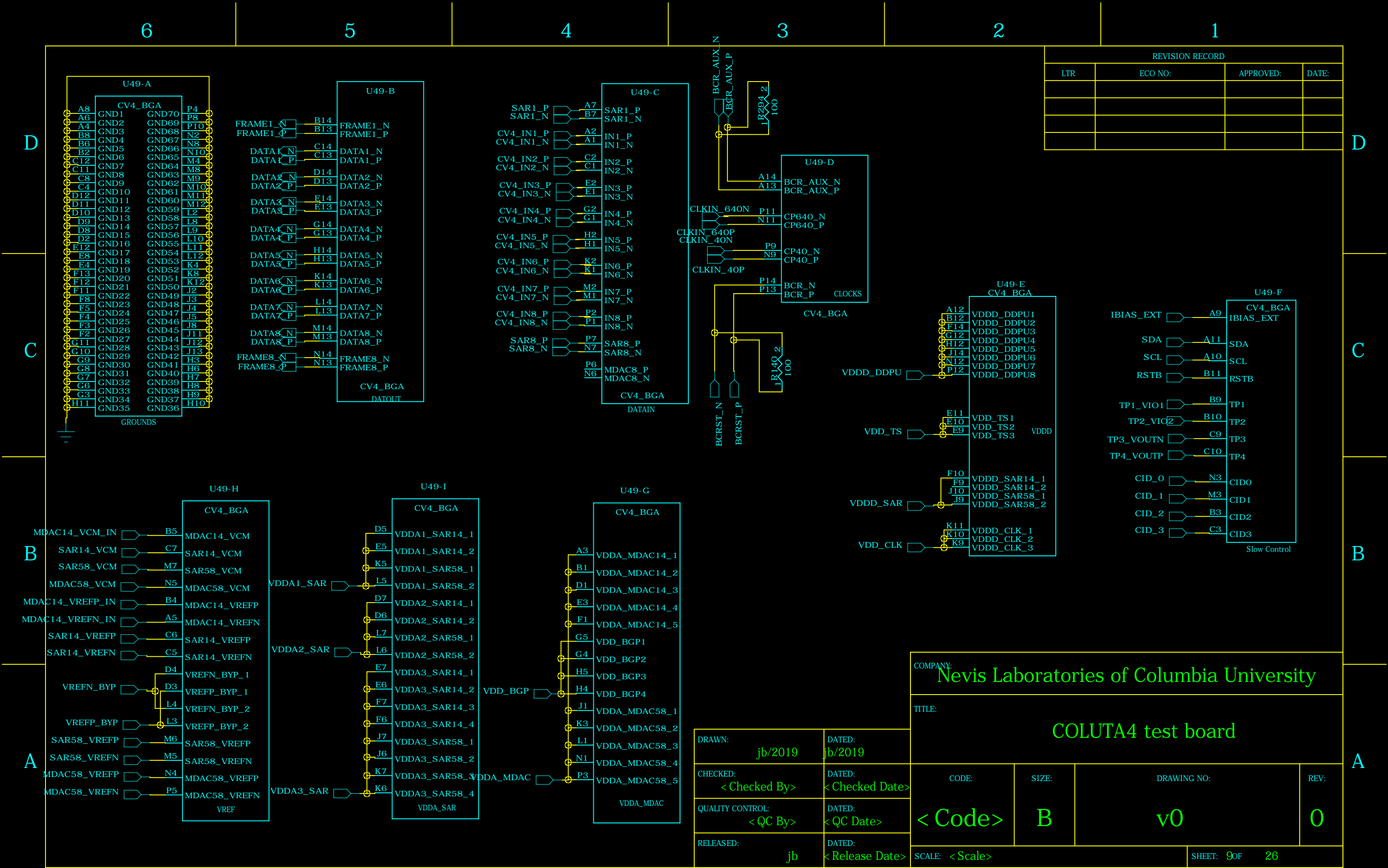
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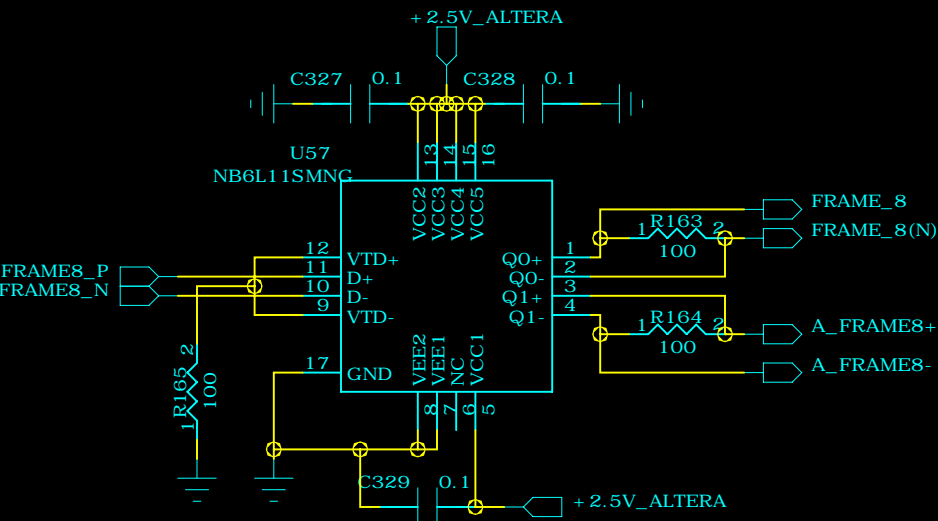
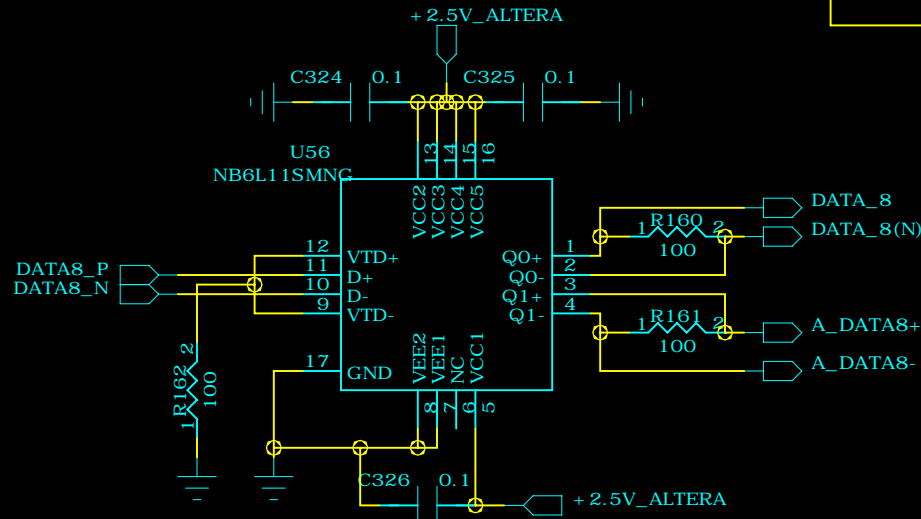
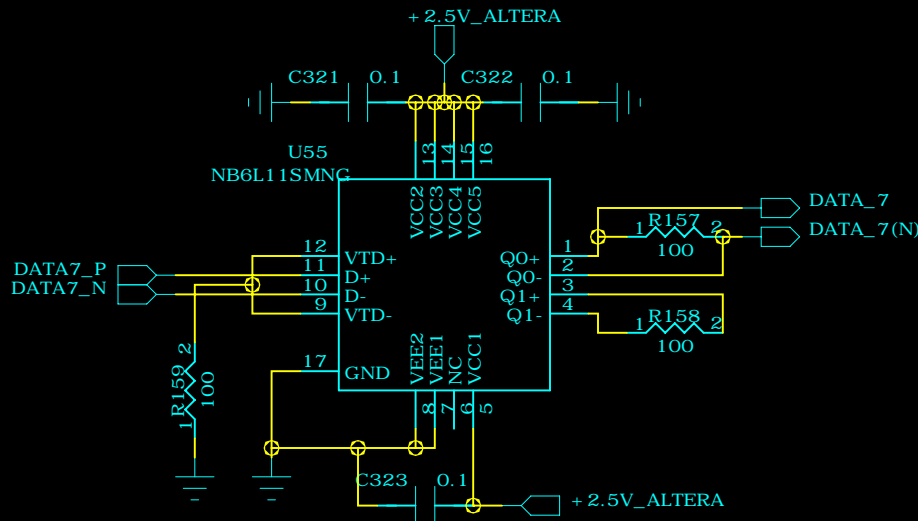
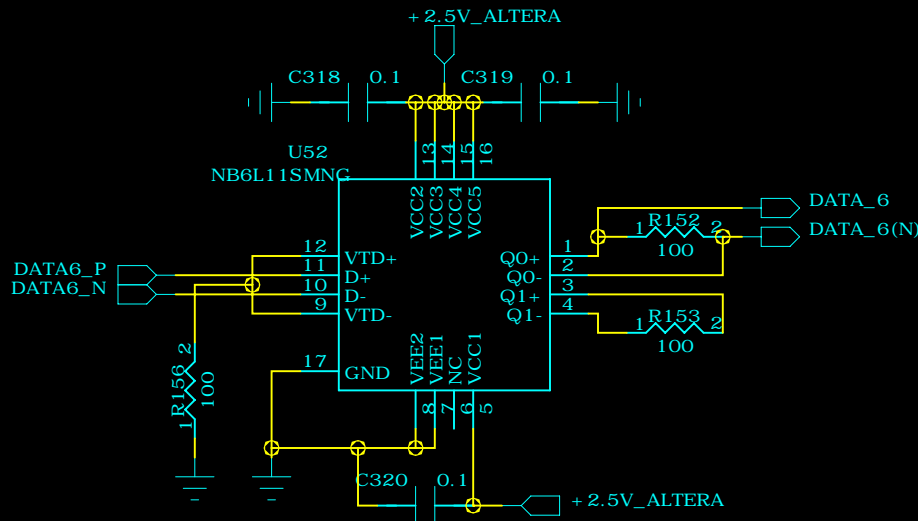
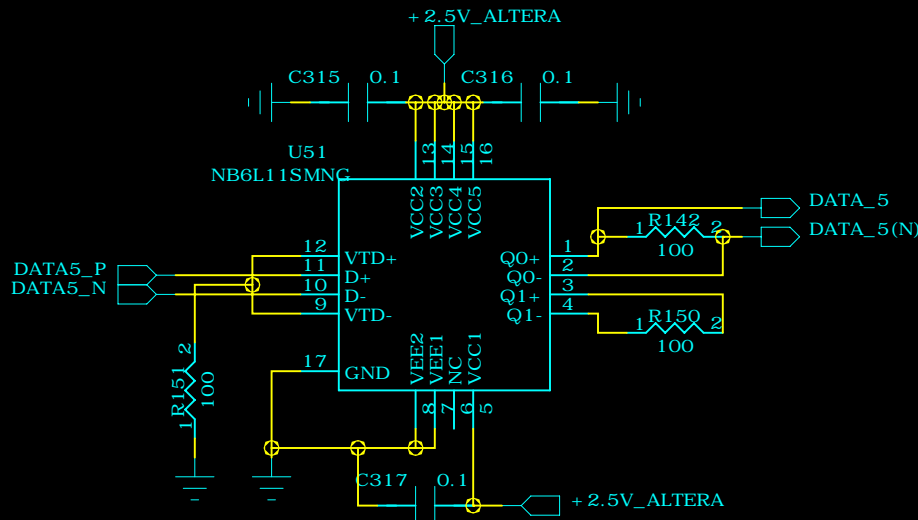
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COMPANY: Nevis Laboratories of Columbia University

TITLE: COLUTA4 test board

DRAWN:	j b/2019	DATED:	j b/2019
CHECKED:	< Checked By >	DATED:	< Checked Date >
QUALITY CONTROL:	< QC By >	DATED:	< QC Date >
RELEASED:	j b	DATED:	< Release Date >

CODE:	SIZE:	DRAWING NO:	REV:
< Code >	B	v0	0
SCALE: < Scale >			SHEET: 10 26

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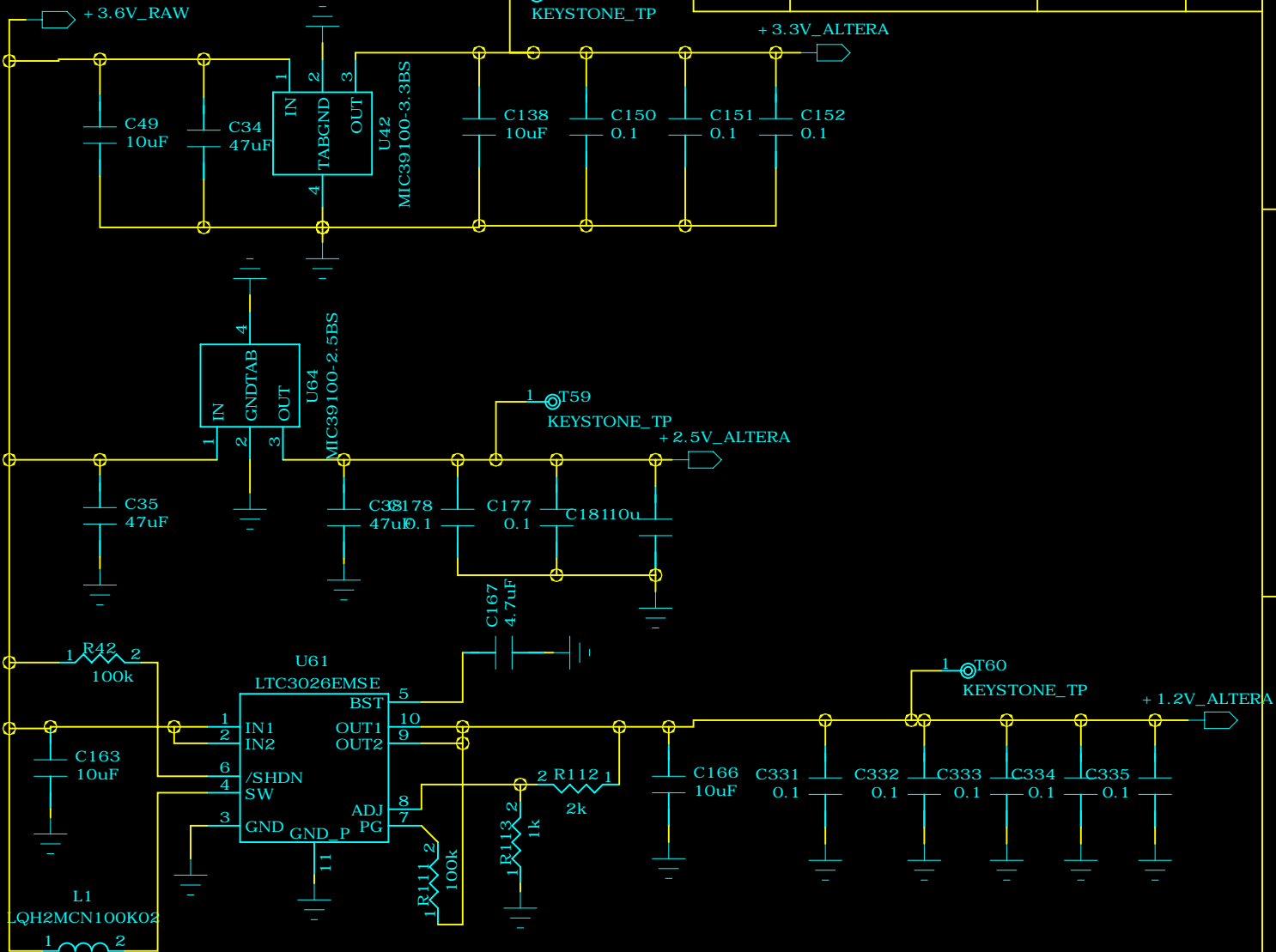
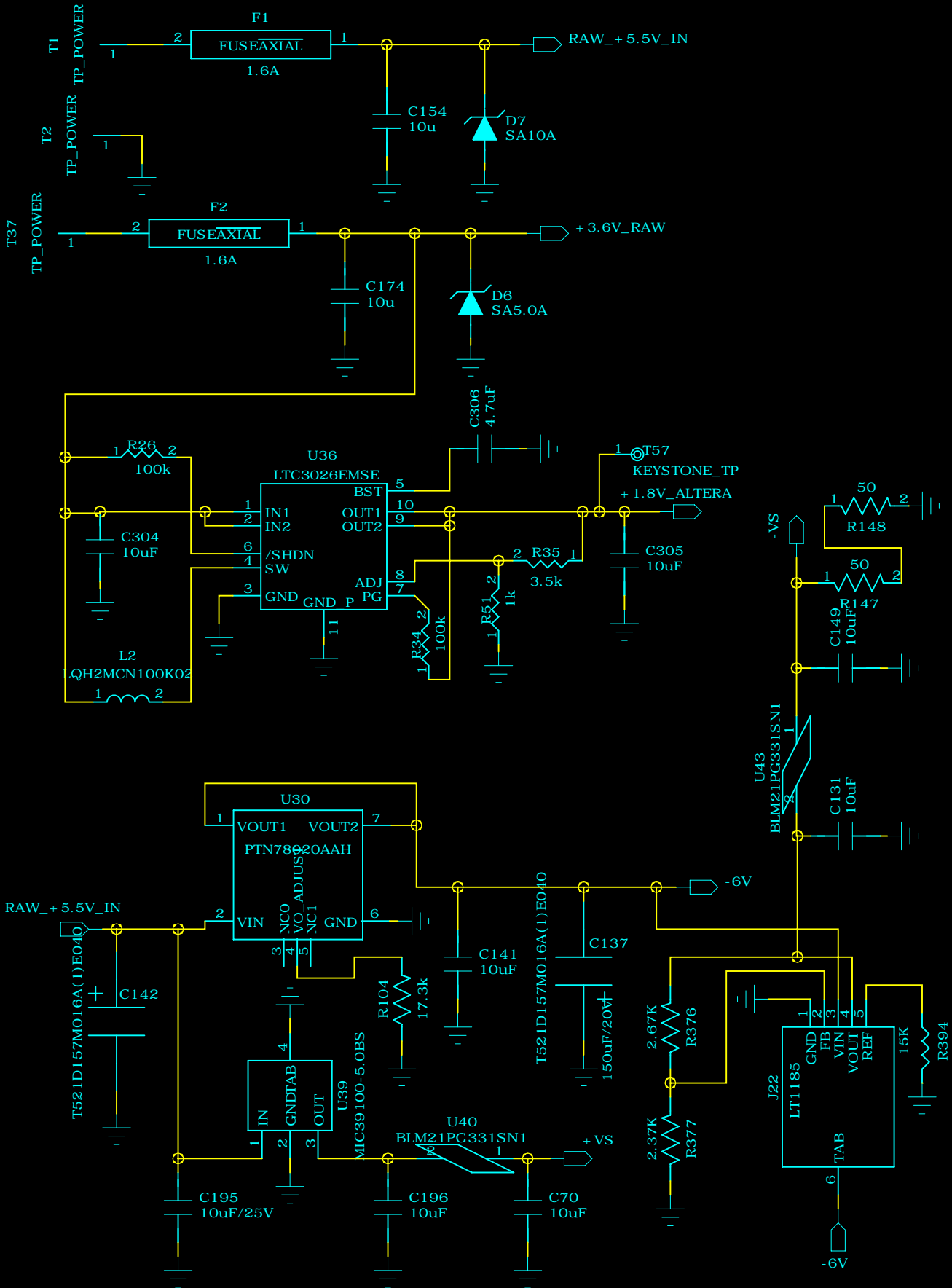
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Nevis Laboratories of Columbia University			
TITLE: COLUTA4 test board			
CODE:	SIZE: B	DRAWING NO: v0	REV: 0
SCALE:			SHEET: 18 26

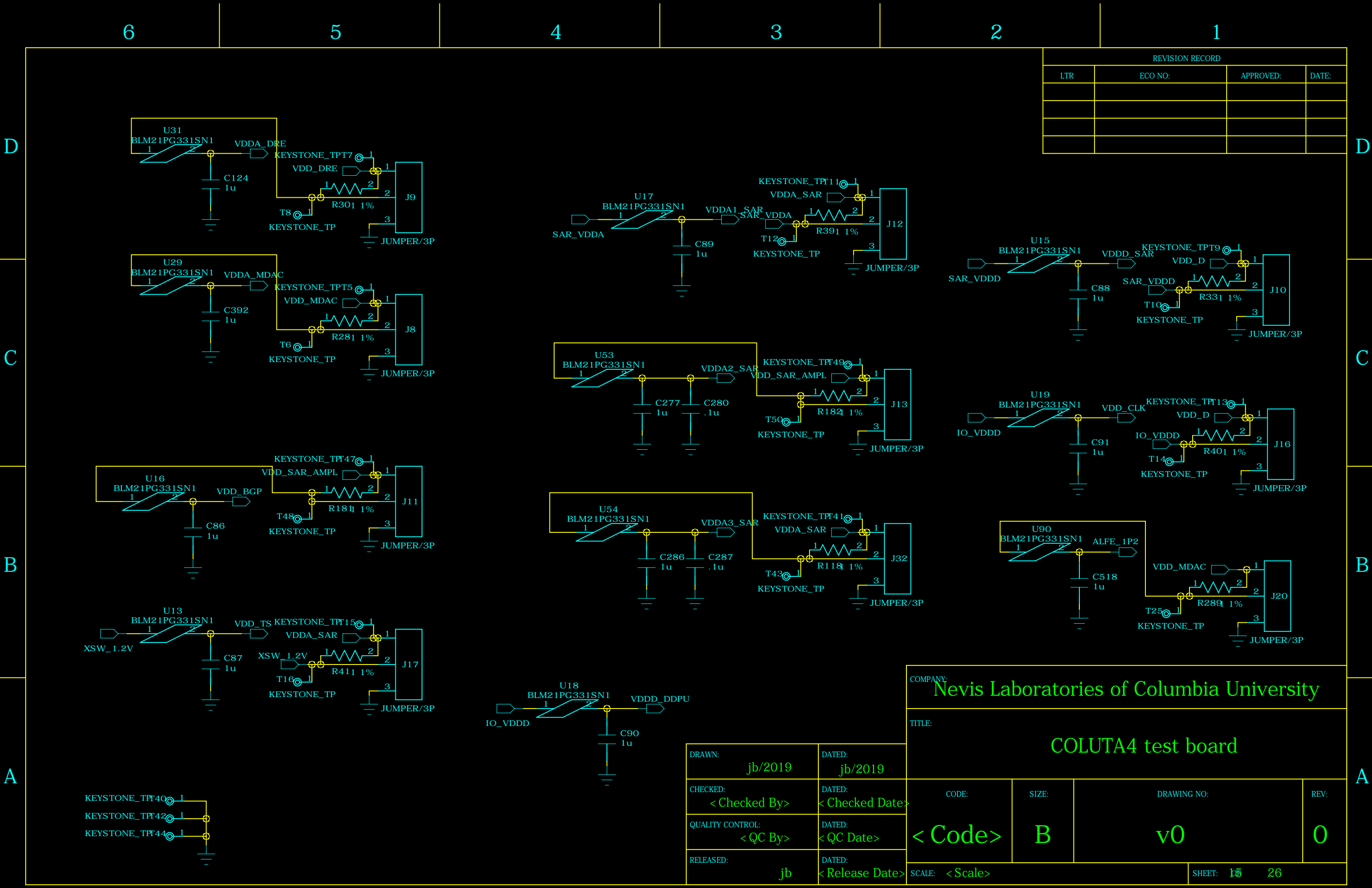
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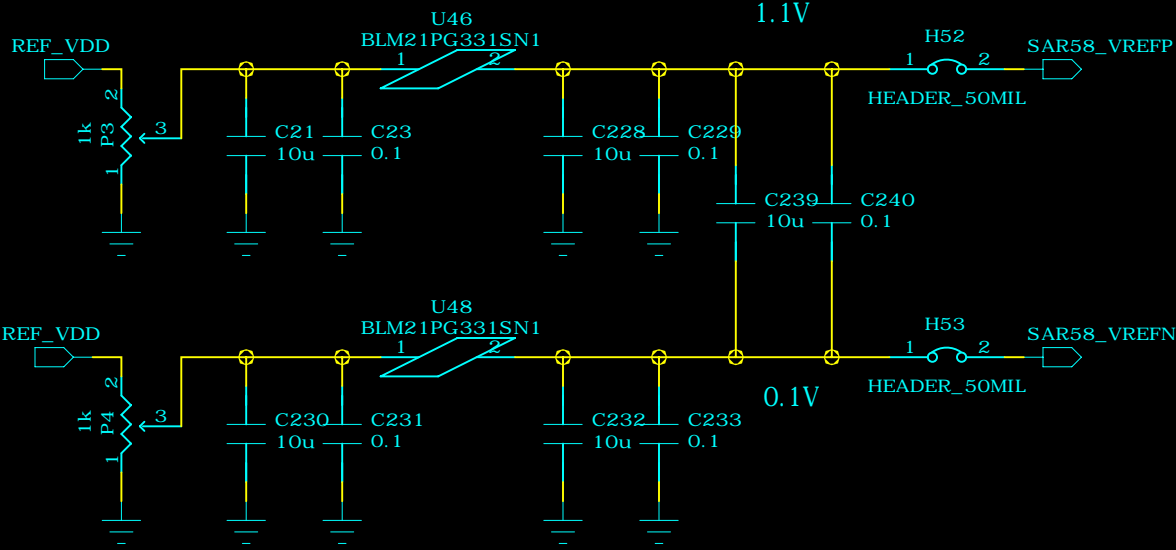
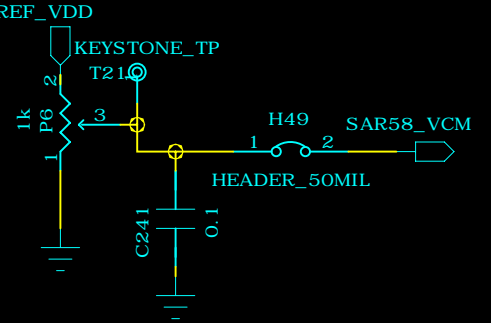
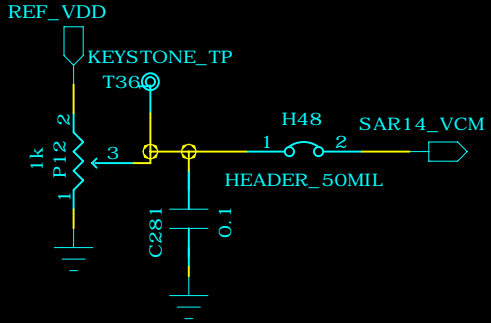
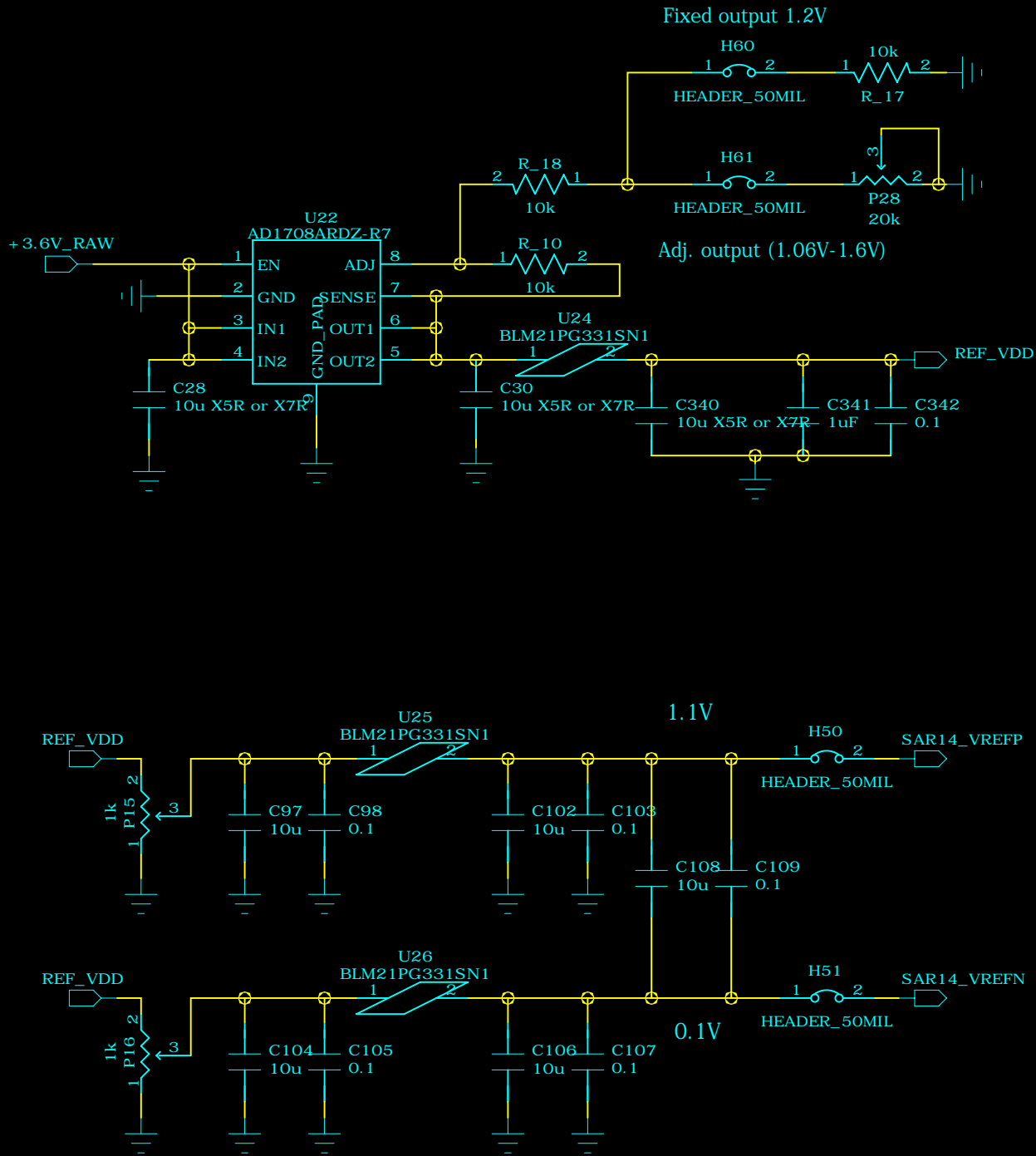


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DRAWN:	j b/2019	DATED:	j b/2019
CHECKED:	< Checked By >	DATED:	< Checked Date >
QUALITY CONTROL:	< QC By >	DATED:	< QC Date >
RELEASED:	j b	DATED:	< Release Date >

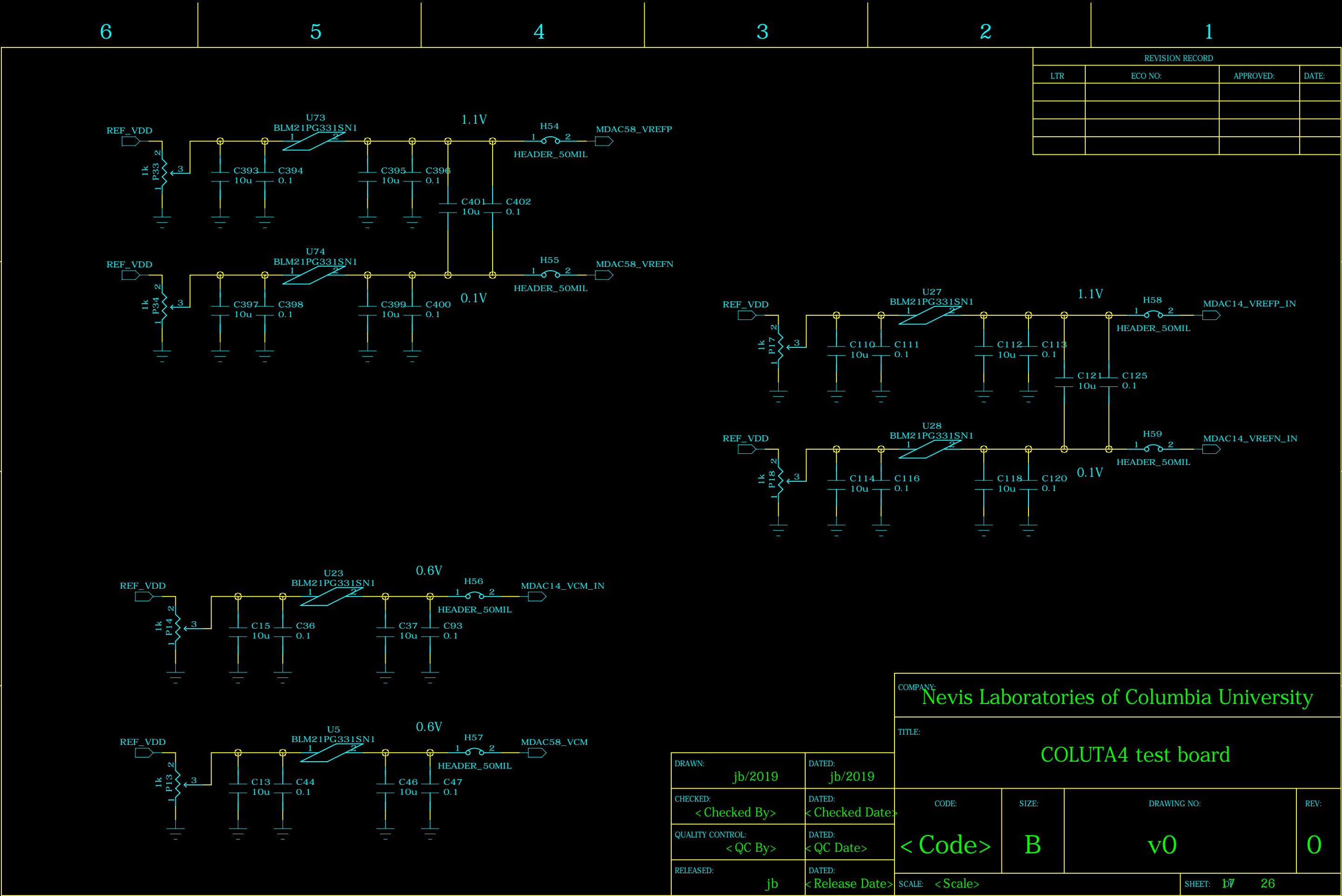
COMPANY: Nevis Laboratories of Columbia University			
TITLE: COLUTA4 test board			
CODE:	SIZE:	DRAWING NO:	REV:
< Code >	B	v0	0
SCALE: < Scale >			SHEET: 10 26

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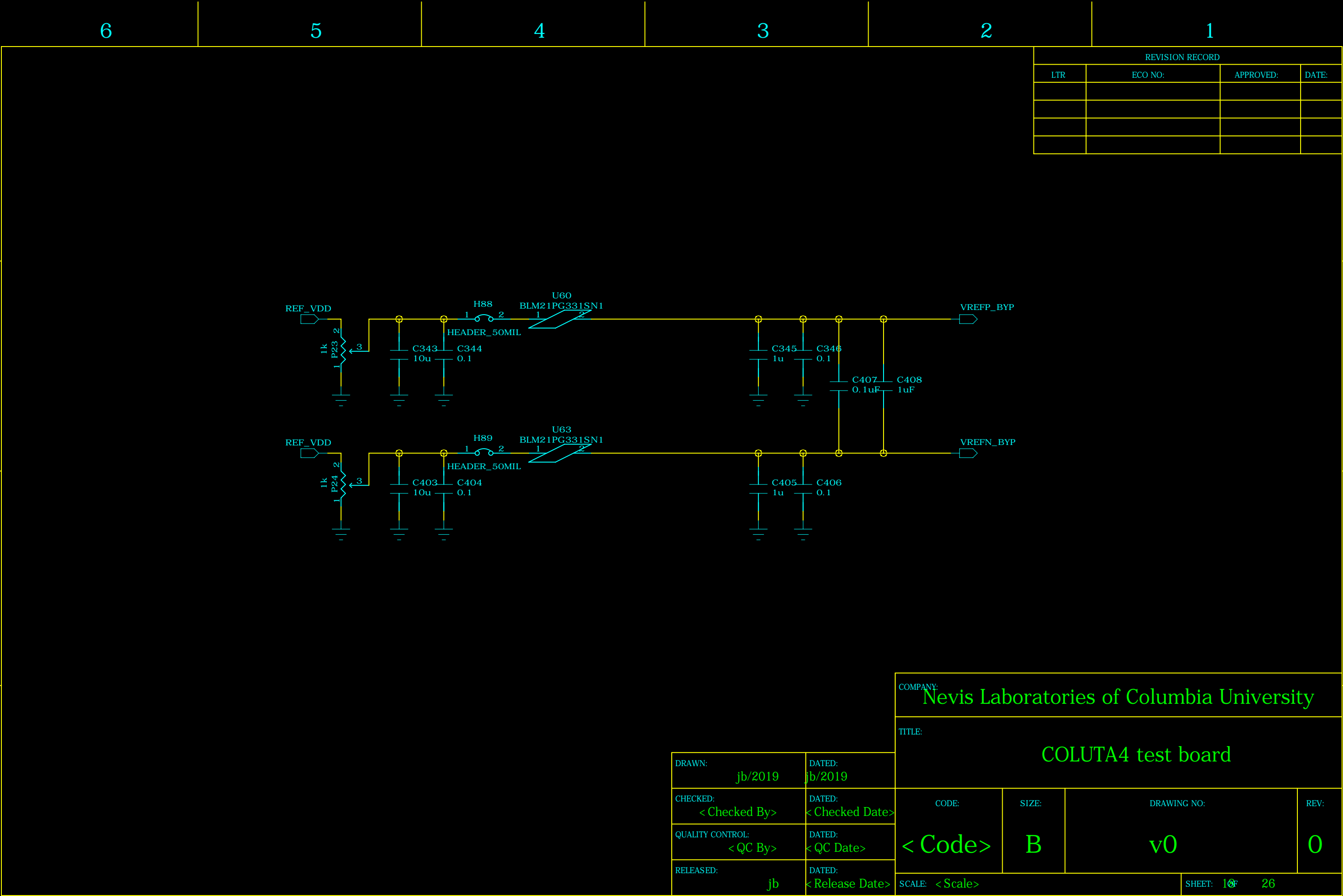
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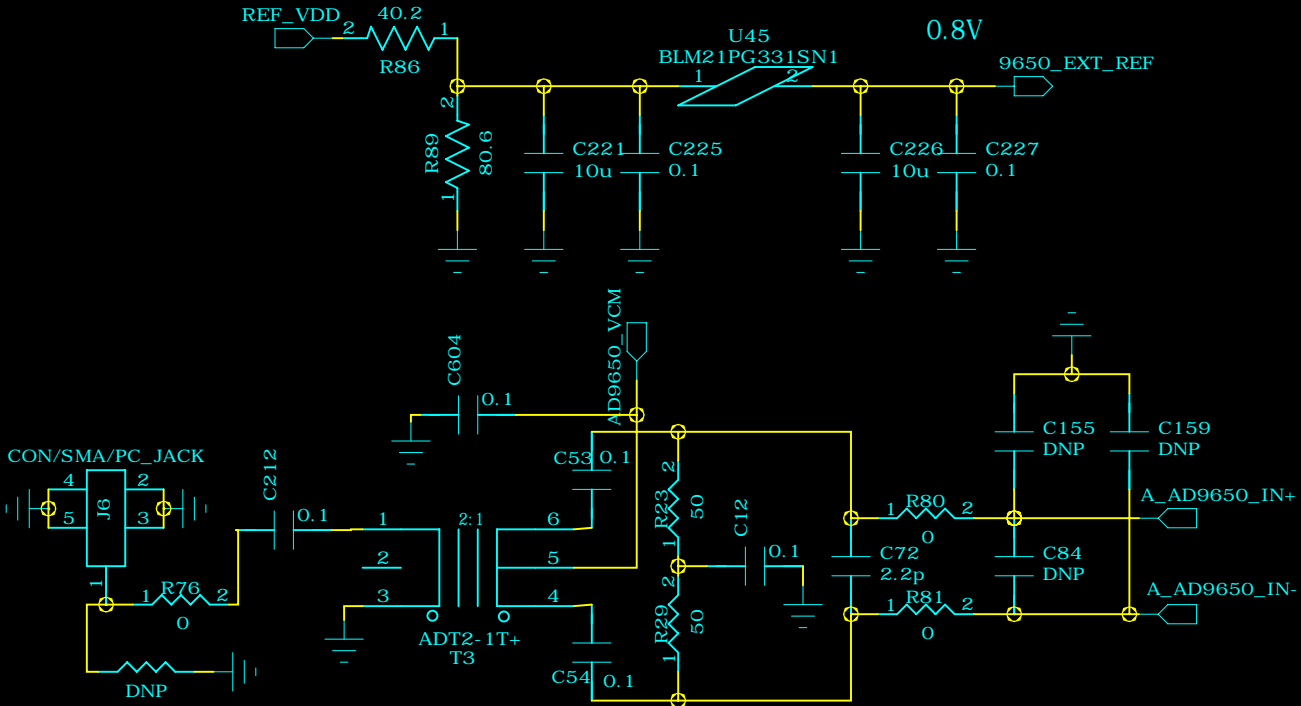
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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COMPANY: Nevis Laboratories of Columbia University

TITLE: COLUTA4 test board

DRAWN: jb/2019	DATED: jb/2019
CHECKED: < Checked By >	DATED: < Checked Date >
QUALITY CONTROL: < QC By >	DATED: < QC Date >
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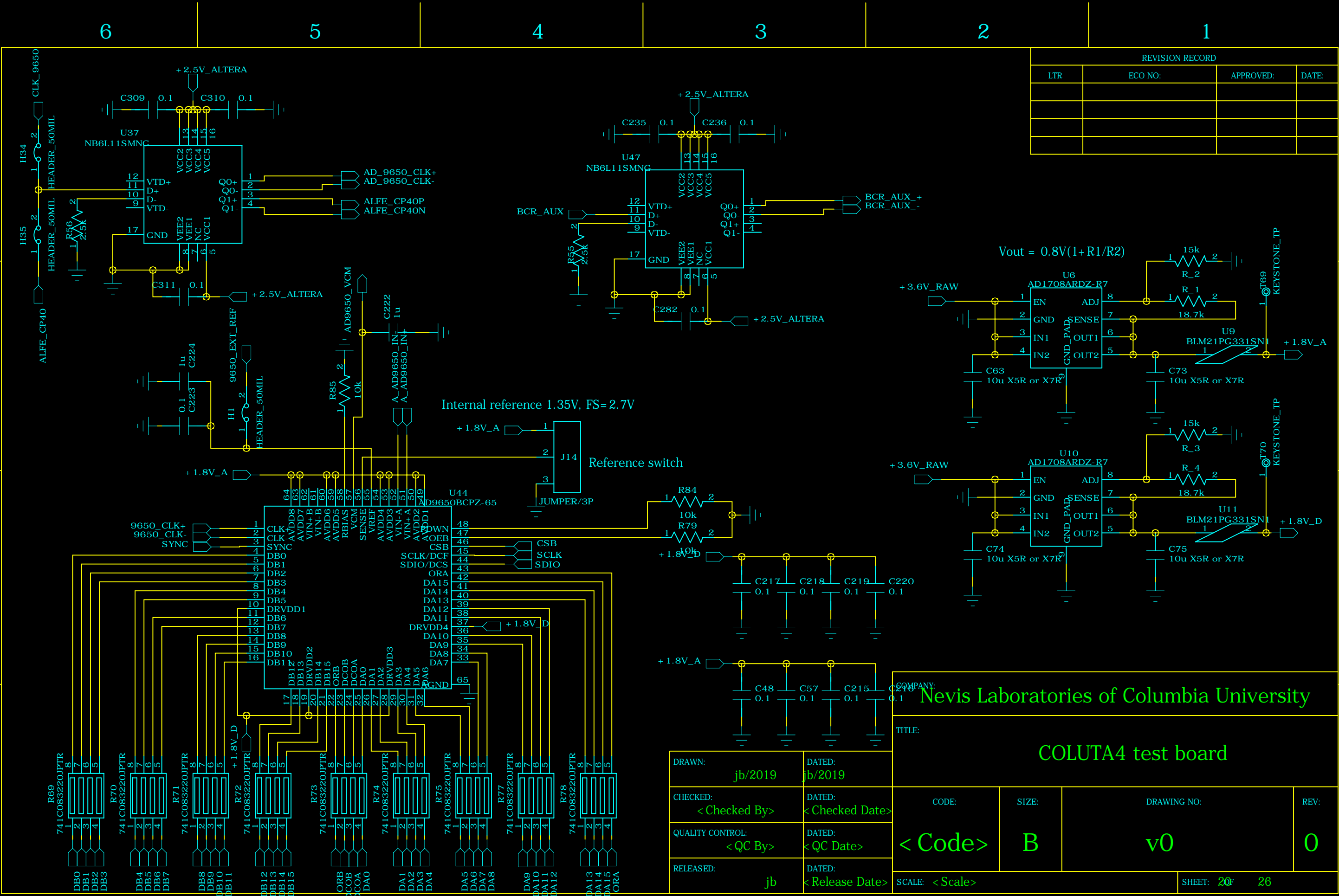
CODE:	SIZE:	DRAWING NO:	REV:
< Code >	B	v0	0
SCALE: < Scale >			SHEET: 10F 26

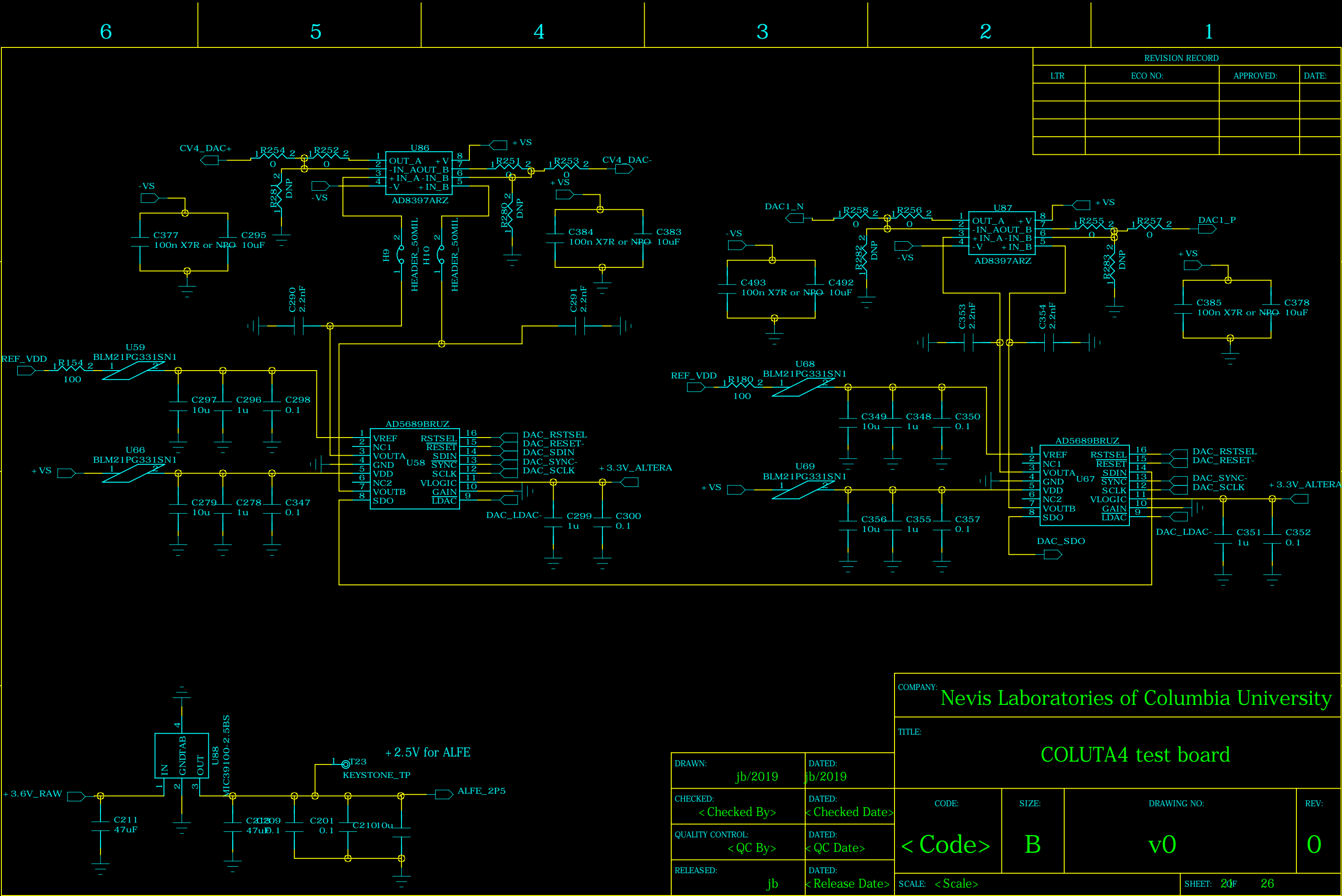
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

DRAWN:	jb/2019	DATED:	jb/2019
CHECKED:	< Checked By >	DATED:	< Checked Date >
QUALITY CONTROL:	< QC By >	DATED:	< QC Date >
RELEASED:	jb	DATED:	< Release Date >

COMPANY: Nevis Laboratories of Columbia University			
TITLE: COLUTA4 test board			
CODE:	SIZE:	DRAWING NO:	REV:
< Code >	B	v0	0
SCALE: < Scale >			SHEET: 20f 26

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REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:

+ 3.6V\_RAW

C410

10uF

C412

47uF

IN

1

TABGND

2

OUT

3

U65

MIC39100-3.3BS

4

C409

10uF

C411

0.1

R241

1k

R242

1k

1

T68

KEYSTONE\_TP

+ 3.3V\_ADC121

DRAWN:

jb/2019

CHECKED:

< Checked By>

QUALITY CONTROL:

< QC By>

RELEASED:

jb

DATED:

jb/2019

DATED:

< Checked Date>

DATED:

< QC Date>

DATED:

< Release Date>

COMPANY:

Nevis Laboratories of Columbia University

TITLE:

COLUTA4 test board

CODE:

< Code>

SIZE:

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DRAWING NO:

v0

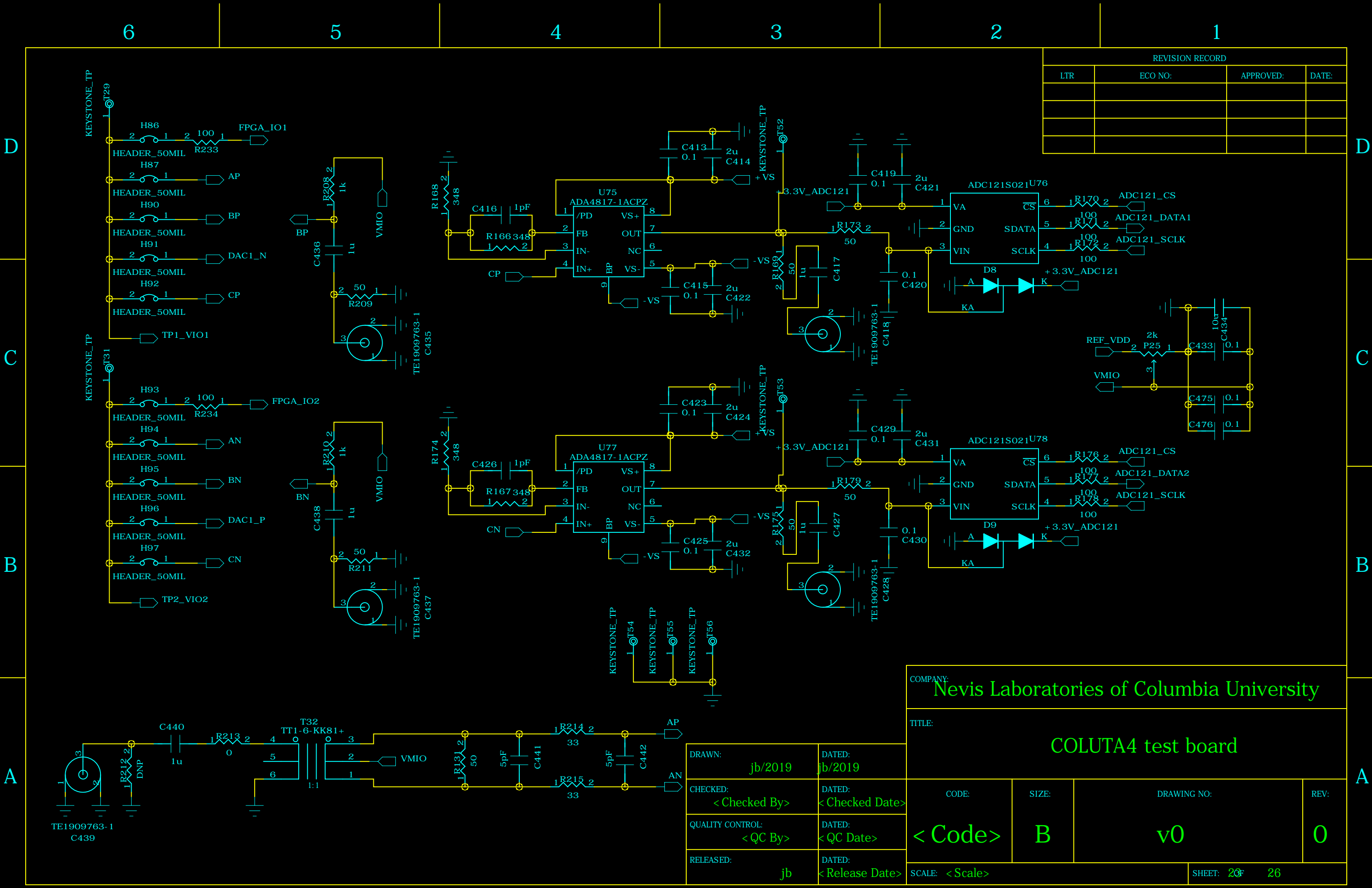
REV:

0

SCALE: < Scale>

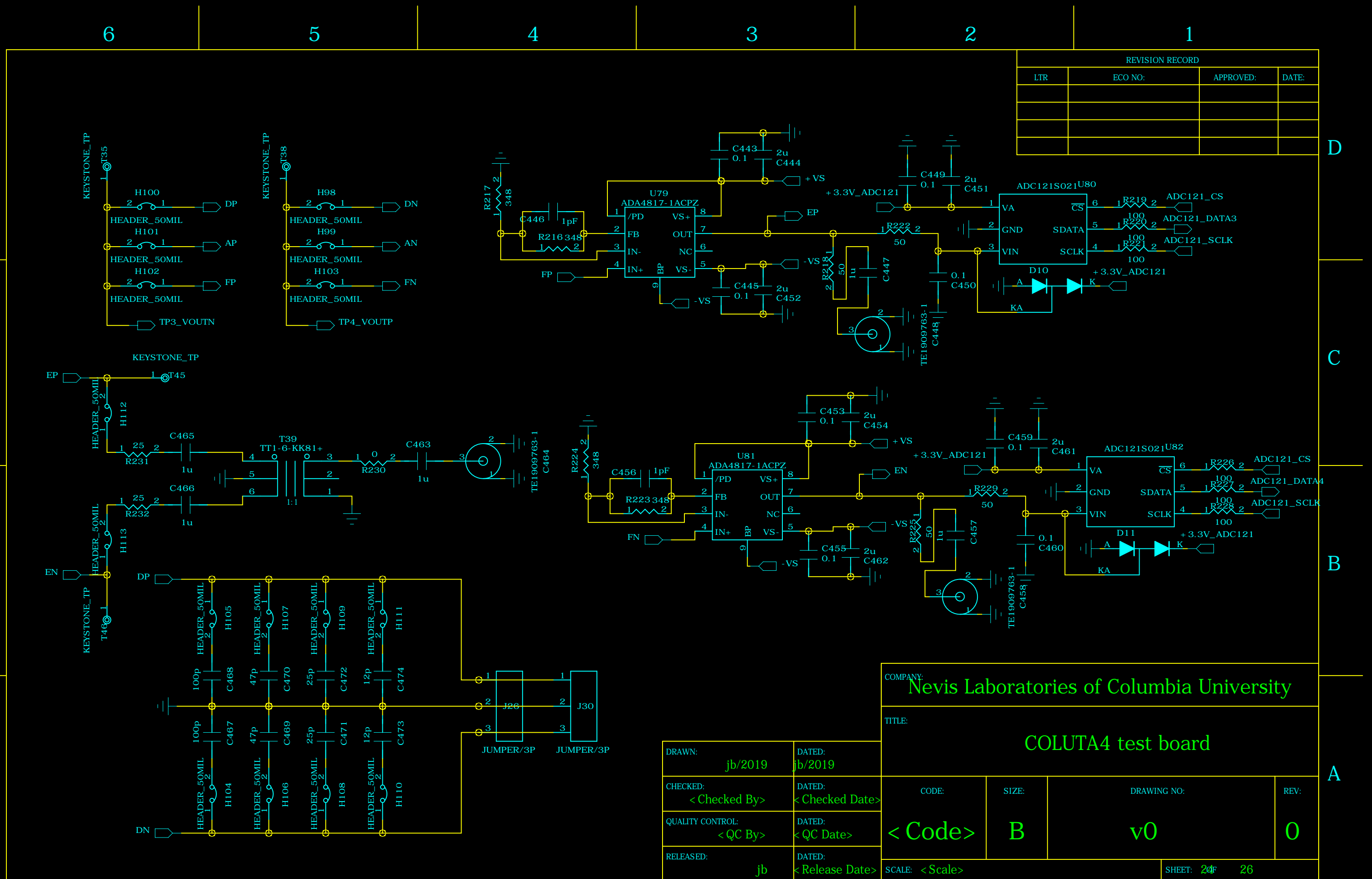
SHEET: 2026





REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Nevis Laboratories of Columbia University			
TITLE: COLUTA4 test board			
CODE: Code	SIZE: B	DRAWING NO: v0	REV: 0
SCALE: <Scale>			SHEET: 20 of 26

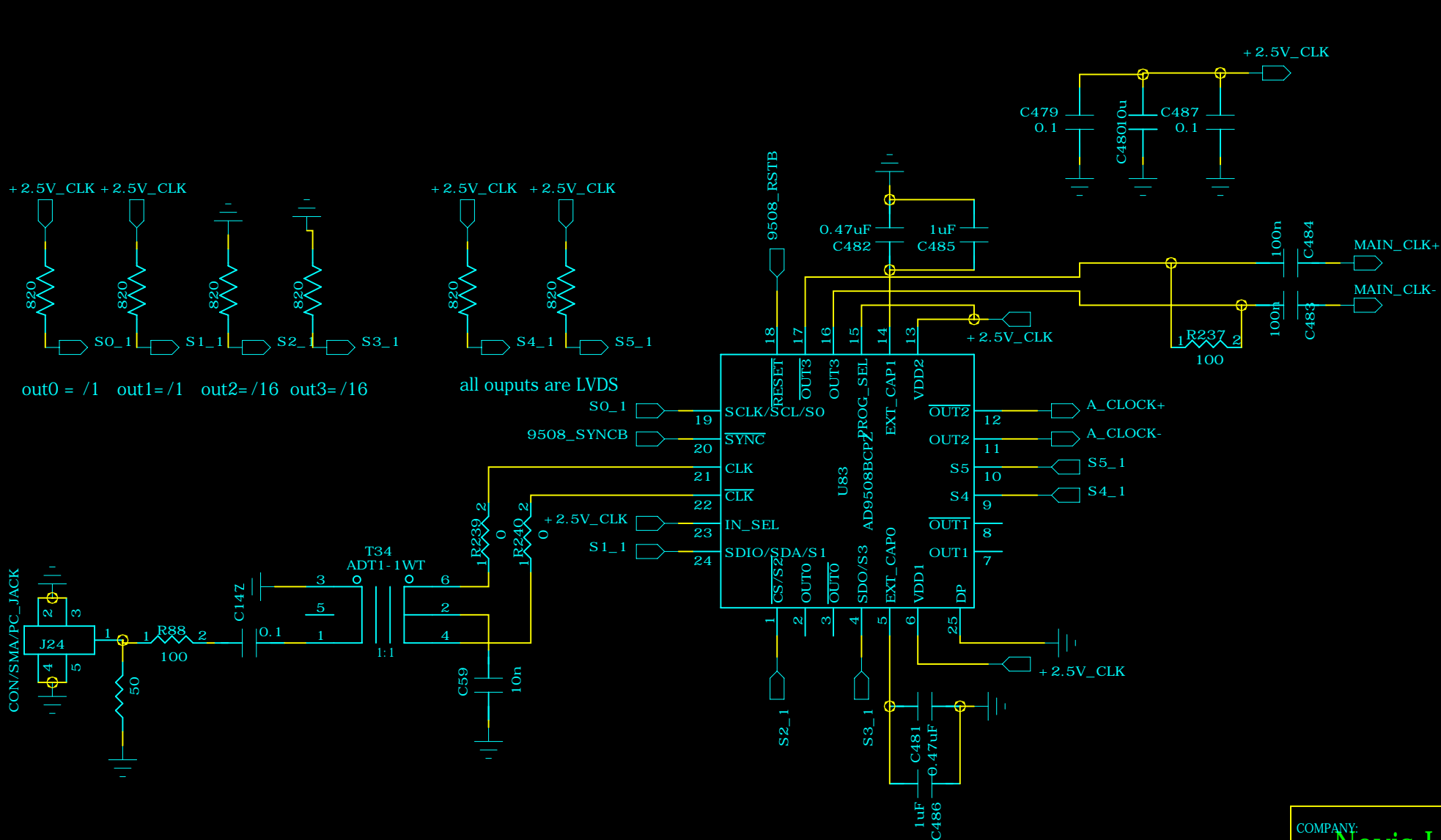


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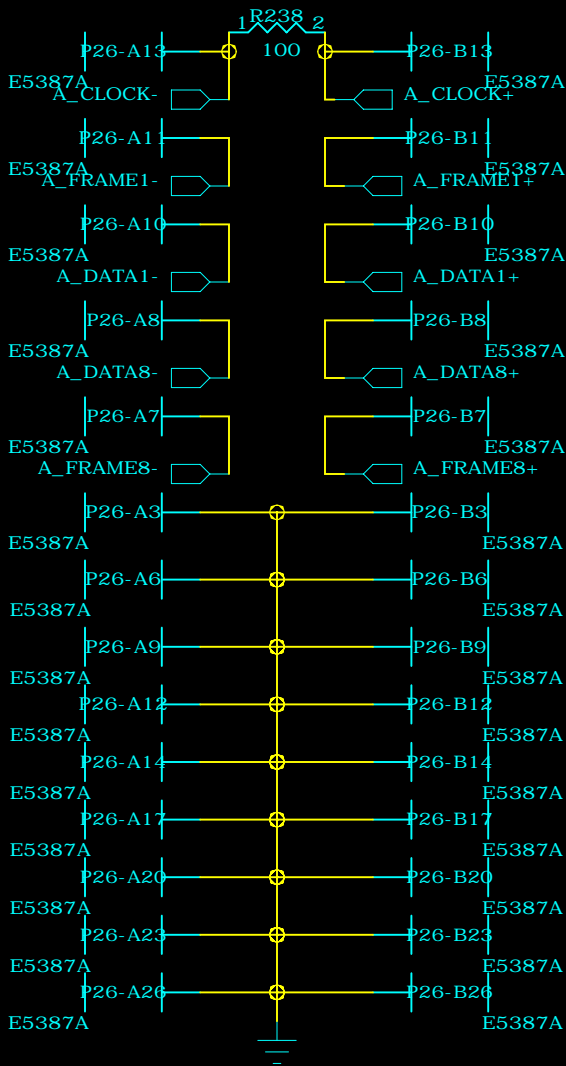
A



out0 = /1 out1=/1 out2=/16 out3=/16

all outputs are LVDS

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Nevis Laboratories of Columbia University

TITLE: COLUTA4 test board

DRAWN:	jb/2019	DATED:	jb/2019
CHECKED:	< Checked By >	DATED:	< Checked Date >
QUALITY CONTROL:	< QC By >	DATED:	< QC Date >
RELEASED:	jb	DATED:	< Release Date >

CODE:	SIZE:	DRAWING NO:	REV:
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SCALE: < Scale >			SHEET: 26 26

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REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:

CON/SMA/PC\_JACK

J27

4

5

2

3

REF\_VDD

P5

2k

1

3

C285

0.1

C289

10u

ADT2-1T+

T27

2:1

6

5

4

1

2

3

R134

86.6

1

2

H36

HEADER\_50MIL

R133

100

1

2

H37

HEADER\_50MIL

R135

86.6

1

2

H115

HEADER\_50MIL

H114

2

1

C598

DNP

SAR8\_P

C597

DNP

SAR8\_N

C599

DNP

CV4\_DAC+

CV4\_DAC-

C602

DNP

C601

DNP

C603

DNP

R138

0

1

2

R139

0

1

2

CON/SMA/PC\_JACK

J25

4

5

2

3

REF\_VDD

P1

2k

1

3

C143

0.1

C157

10u

ADT2-1T+

T24

2:1

6

5

4

1

2

3

R53

0

1

2

C176

0.1uF

R54

100

1

2

R61

86.6

1

2

H18

HEADER\_50MIL

R62

86.6

1

2

H19

HEADER\_50MIL

H20

2

1

H21

HEADER\_50MIL

C170

DNP

SAR1\_P

C158

DNP

SAR1\_N

C175

DNP

CV4\_DAC+

CV4\_DAC-

C180

DNP

C179

DNP

C234

DNP

F98

0

1

2

F99

0

1

2

DRAWN:

jb/2019

CHECKED:

<Checked By>

QUALITY CONTROL:

<QC By>

RELEASED:

jb

DATED:

jb/2019

DATED:

<Checked Date>

DATED:

<QC Date>

DATED:

<Release Date>

COMPANY:

Nevis Laboratories of Columbia University

TITLE:

COLUTA4 test board

CODE:	SIZE:	DRAWING NO:	REV:
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SHEET: 2626